

ZZZ8
DAZ@
DAZ04300100
PCB

ZZZ1
DA2@
MAIN BOARD
DA600007E10
PCB

ZZZ2
DA2@
E board
DA40000A910
PCB

ZZZ3
DA2@
FP board
DA600007D10
PCB

ZZZ4
DA2@
Function board
DA40000AA10
PCB

ZZZ5
DA2@
USB board
DA40000AB10
PCB

ZZZ6
DA2@
IO board
DA40000AC10
PCB

ZZZ7
DA2@
Power board
DA40000AD10
PCB

Compal Confidential

KAQB0 M/B Schematics Document

Intel Penryn Processor with Cantiga + DDRIII + ICH9M

2009-03-27

REV:1.0

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Title	SCHEMATIC, MB A5011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number 401673	Rev B
				Date: Thursday, April 30, 2009	Sheet 1 of 43

Compal Confidential

Model Name : KAQB0

File Name : LA5011P

P/N:DA60000B110

Fan Control
page4

Intel Montevina Processor

uPGA-478 Package
(Socket P) page4, 5, 6

Thermal Sensor
EMC1402-1-ACZL
page4

Clock Generator
ICS9LPRS387
page15

LCD Conn.
page23

CRT Conn.
page24

H_A#(3..35)

FSB
800/1066MHz

H_D#(0..63)

Intel Cantiga

uFCBGA-1329
page7, 8, 9, 10, 11, 12

DDR3 800 MHz 1.5/0.75V

Dual Channel

DDR3-SO-DIMM X2

BANK 0, 1, 2, 3 page13, 14

5 IN 1

1394 Conn.

HDMI Conn.
page25

TMD5

multiplex
PI3LVD1012

LVDS

multiplex
PI3V512QE

LVDS

MXM III VGA/B
page21

PCI-Express
16X

LS-501A
HDMI board
7318 Level shift
page 21

PCI-Express

PCI-Express

DMI

C-Link

Intel ICH9-M

BGA-676

page16, 17, 18, 19

USB Bus

Bluetooth
Conn
page32

LS-5015
Finger Print
SS801U
page32

CMOS
Camera 1.3M
page23

USB conn x3
3.3V 48MHz
page29

LS-5017

New Card
Socket
page29

MINI Card 1,2
WLAN TV tuner
page20

LAN(GbE)
BROADCOM
BCM5764
page26

RJ45
page25

port 0

S-ATA

SATA HDD
Conn.
page20

port 2

E-SATA
Conn.
page20

port 3

CDROM
CONN
page20

ENE KB926
page31

LPC BUS

Touch Pad
page32

Int.KBD
page31

BIOS
page30

3.3V 24.576MHz/48Mhz

HD Audio

GMCH HDA
page7

UMA SKU

HDA Codec
ALC889X
page27

MXM SPDIF
page27

APA2051
Audio AMP
page28

APA3011
Audio AMP
page28

Phone Jack x3
page28

Subwoofer
page28

RTC CKT.
page17

Power On/Off CKT.
page33

DC/DC Interface CKT.
page34

Power Circuit DC/DC
page35, 36, 37, 38, 39, 40, 41

LS-5011
Backlight sensor Conn
ENE-SB3520
page33

LS-5012
Launch sensor Conn
ENE-SB3520
page33

LS-5013
Media sensor Conn
CYPRESS-CY8C20434
page33

LS-5014
VR/B Conn
SW EC05E1210401 2
LEVEL ALPS
page28

LS-5015
Finger Print
SS801U
page32

LS-5016
USB & TV Conn
page29

LS-5018
POWER SAVING Conn
page32

LS-5019
Touch pad Button Conn
page32

LS-5017
MINI Card 1,2
WLAN TV tuner
page20

LS-501A
HDMI board
7318 Level shift
page 21

Security Classification	Compal Secret Data			Title		
Issued Date	2007/09/20	Deciphered Date	2008/09/20	Rev	Document Number	Rev
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			401673	Thursday, April 30, 2009	Sheet 2 of 43	B

Compal Electronics, Inc.		
SCHEMATIC, MB A5011		
Date	Thursday, April 30, 2009	Sheet 2 of 43

power plane State	+B	+5VALW +3VALW	+1.5V	+5VS +3VS +1.5VS +0.75V +VCCP +CPU_CORE +1.8VS
S0	0	0	0	0
S1	0	0	0	0
S3	0	0	0	X
S5 S4/AC	0	0	X	X
S5 S4/ Battery only	0	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

Symbol Note :

 : means Digital Ground

 : means Analog Ground

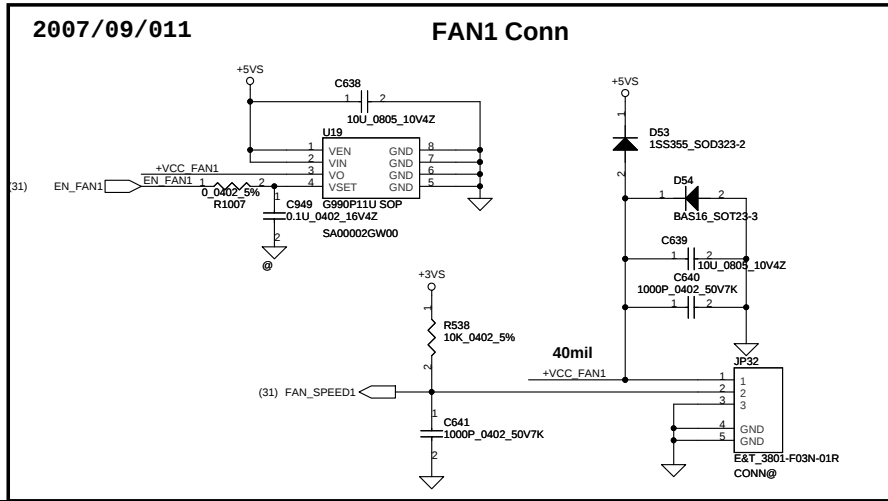
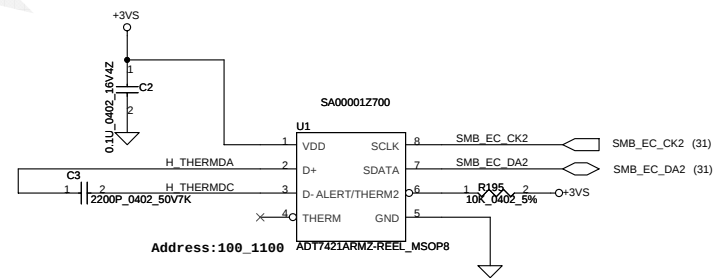
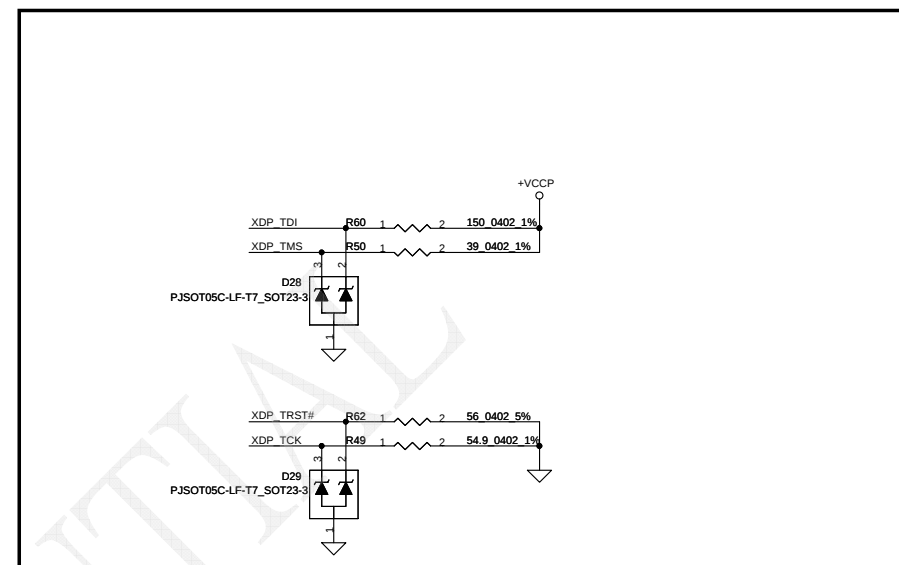
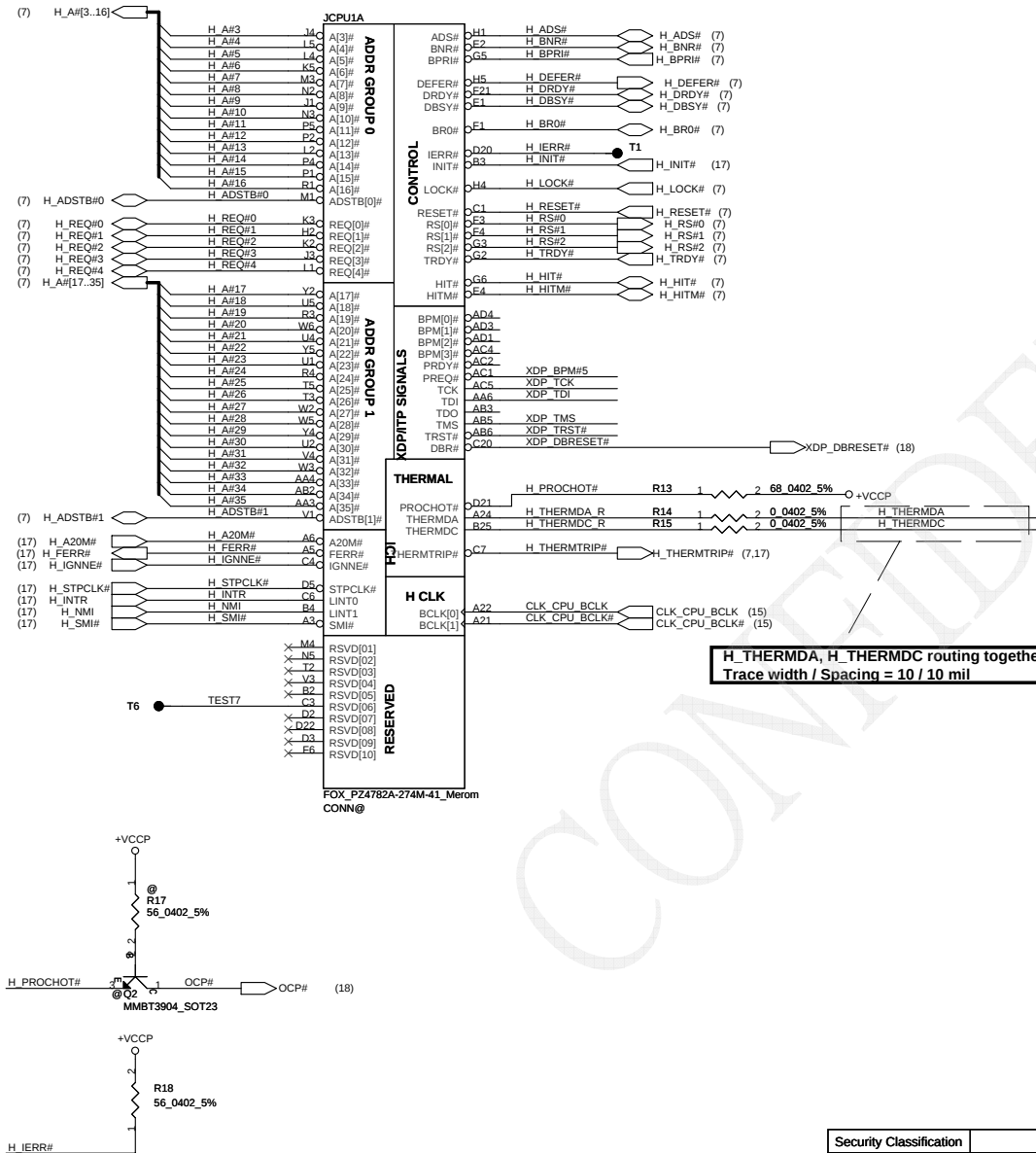
@ : means just reserve , no build
DEBUG@ : means just reserve for debug.

SMBUS Control Table

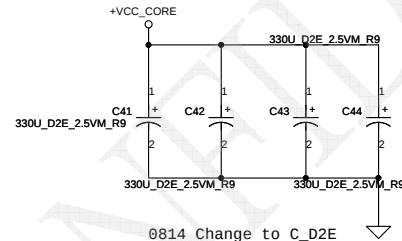
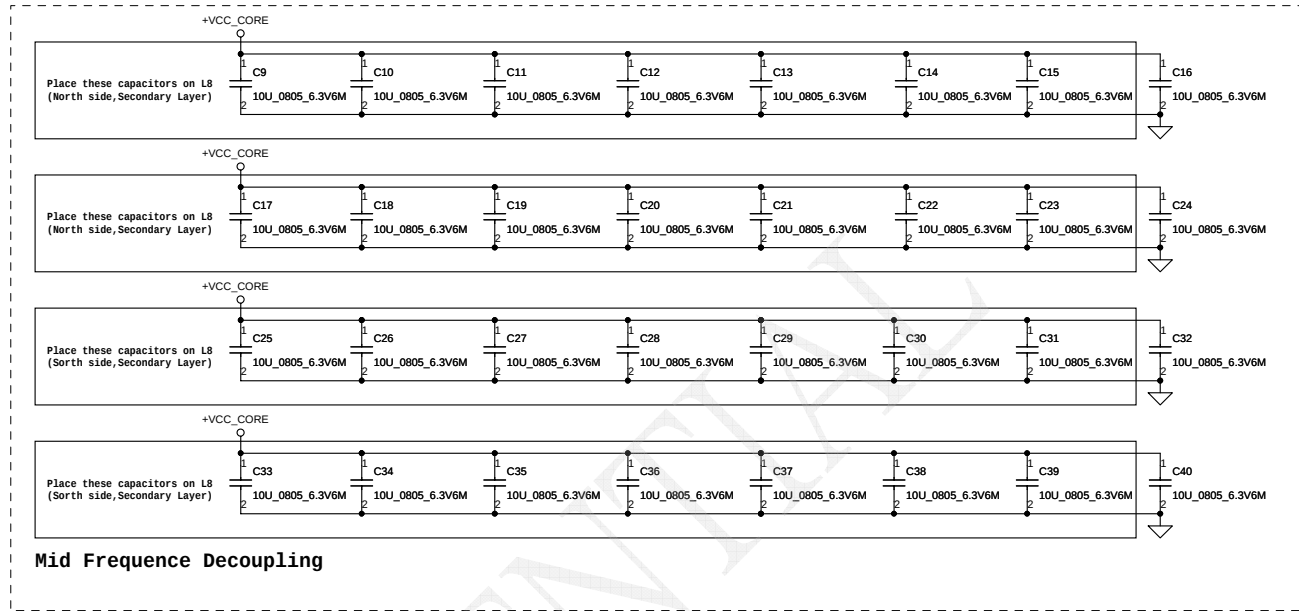
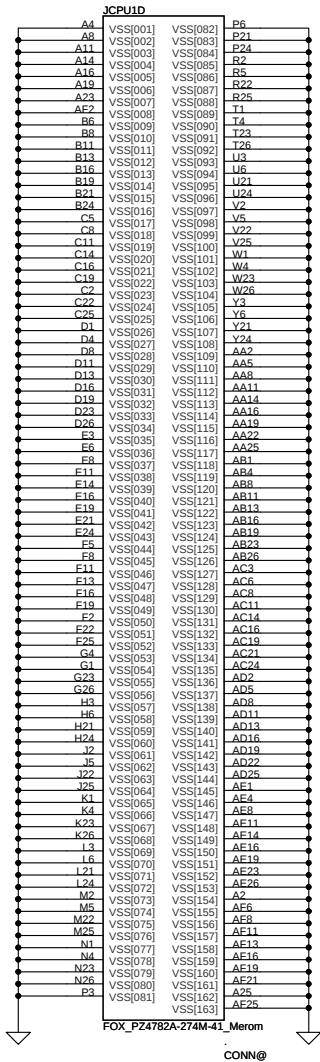
	SOURCE	INVERTER	BATT	SERIAL EEPROM	THERMAL SENSOR (CPU)	SODIMM	CLK CHIP	MINI CARD	LCD
SMB_EC_CK1 SMB_EC_DA1	KB926	X	V	V	X	X	X	X	X
SMB_EC_CK2 SMB_EC_DA2	KB926	X	X	X	V	X	X	X	X
SMB_CK_CLK1 SMB_CK_DAT1	ICH9	X	X	X	X	V	V	V	X
LCD_CLK LCD_DAT	Cantiga	X	X	X	X	X	X	X	V

I2C / SMBUS ADDRESSING

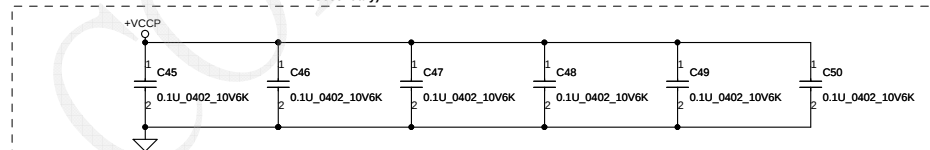
DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	10100000
DDR SO-DIMM 1	A4	10100100
CLOCK GENERATOR (EXT.)	D2	11010010



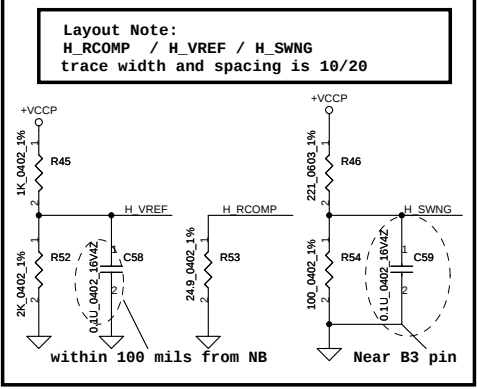
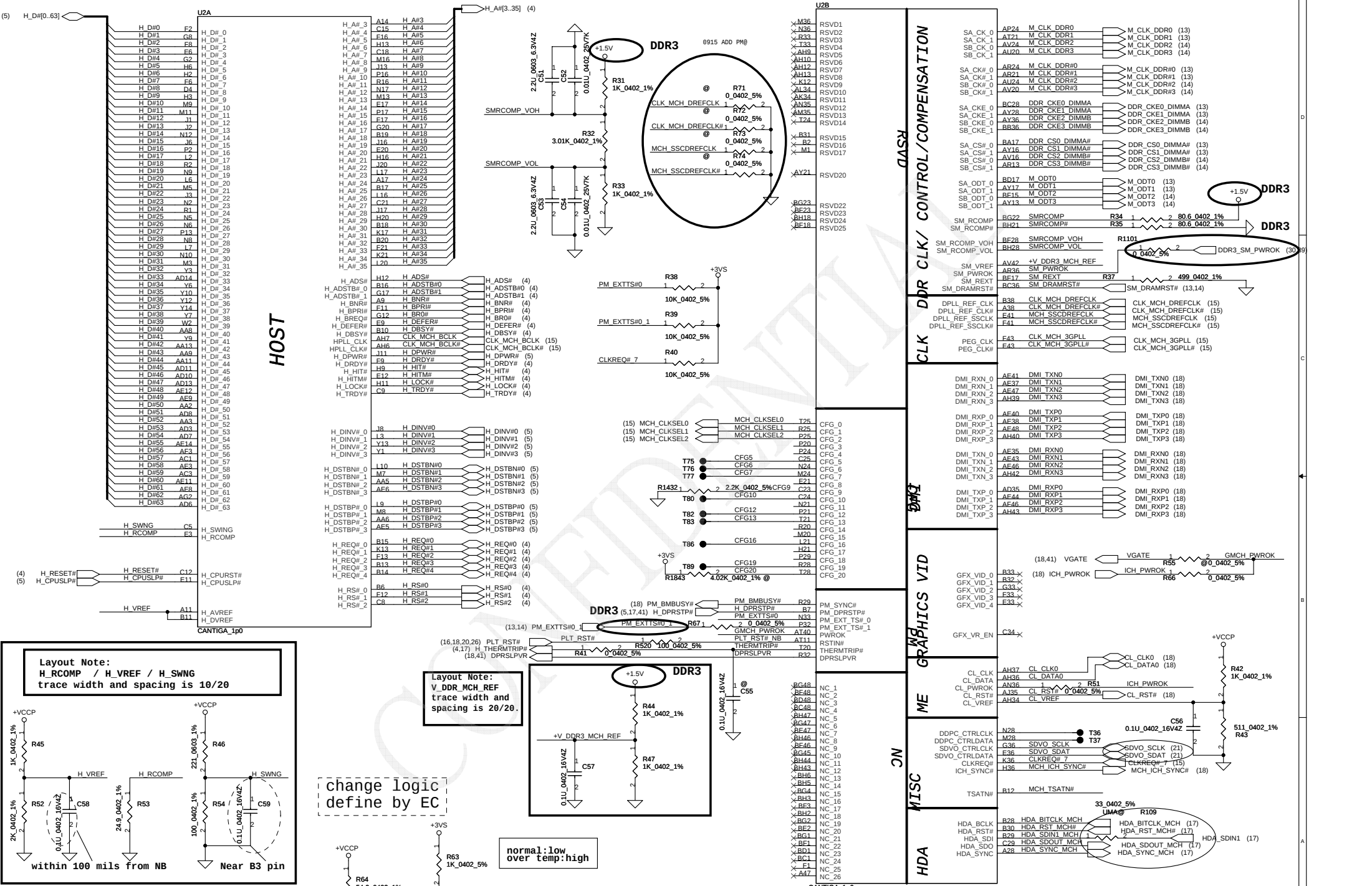
Security Classification				Compal Secret Data				Title			
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Document Number	401673	Rev	B	Document Number	401673	Rev	B
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.											
Date: Thursday, April 30, 2009				Sheet 4 of 43				Compal Electronics, Inc.			
								SCHEMATIC, MB A5011			



Place these inside socket cavity on L8 (North side Secondary)



Security Classification		Compal Secret Data		Title	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Document Number	401673
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev	B
Date: Thursday, April 30, 2009				Sheet	6 of 43



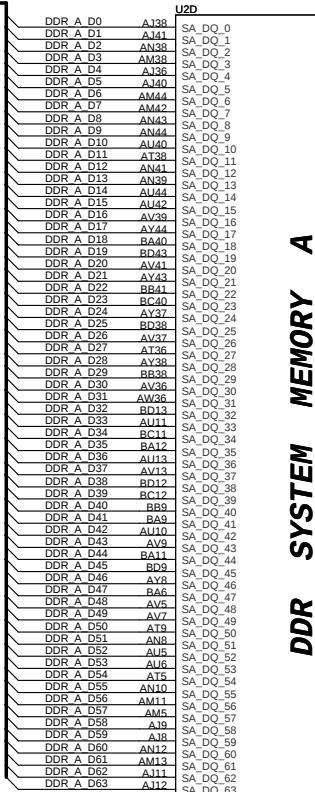
Layout Note:
V_DDR_MCH_REF
trace width and
spacing is 20/20.

change logic
define by EC

normal: low
over temp: high

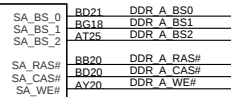
Security Classification		Compal Secret Data				Compal Electronics, Inc.							
Issued Date		2007/09/29		Deciphered Date		2007/09/29		Title					
								SCHEMATIC, MB A5011					
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OR DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.								Customer		Document Number		Rev B	
								401673					
								Date:		Thursday, April 30, 2009		Sheet 7 of 43	

(13) DDR_A_D[0..63]

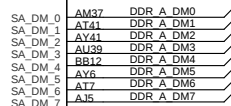


DDR SYSTEM MEMORY A

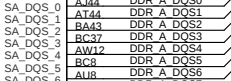
CANTIGA_1p0



DDR_A_DM[0..7] (13)



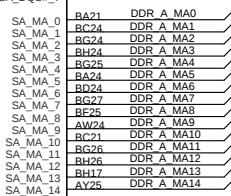
DDR_A_DQS[0..7] (13)



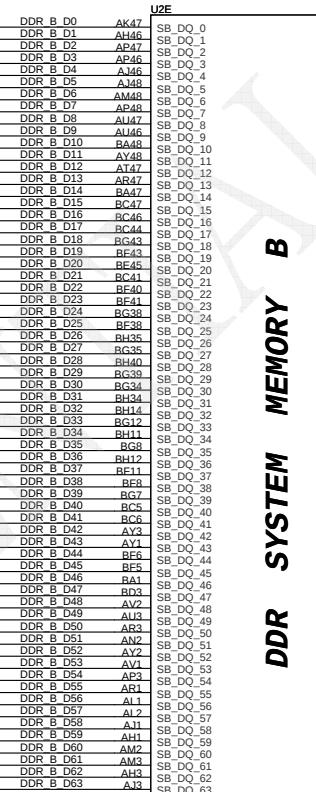
DDR_A_DQS#[0..7] (13)



DDR_A_MA[0..14] (13)

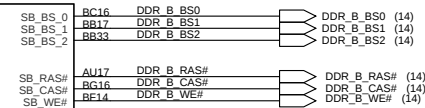


(14) DDR_B_D[0..63]



DDR SYSTEM MEMORY B

CANTIGA_1p0



DDR_B_BS0 (14)

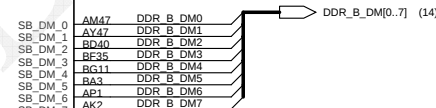
DDR_B_BS1 (14)

DDR_B_BS2 (14)

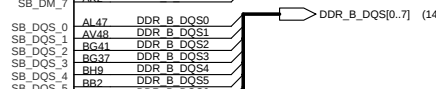
DDR_B_RAS# (14)

DDR_B_CAS# (14)

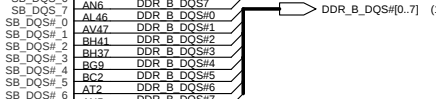
DDR_B_WE# (14)



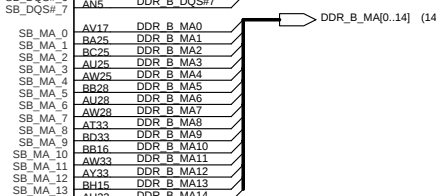
DDR_B_DM[0..7] (14)



DDR_B_DQS[0..7] (14)

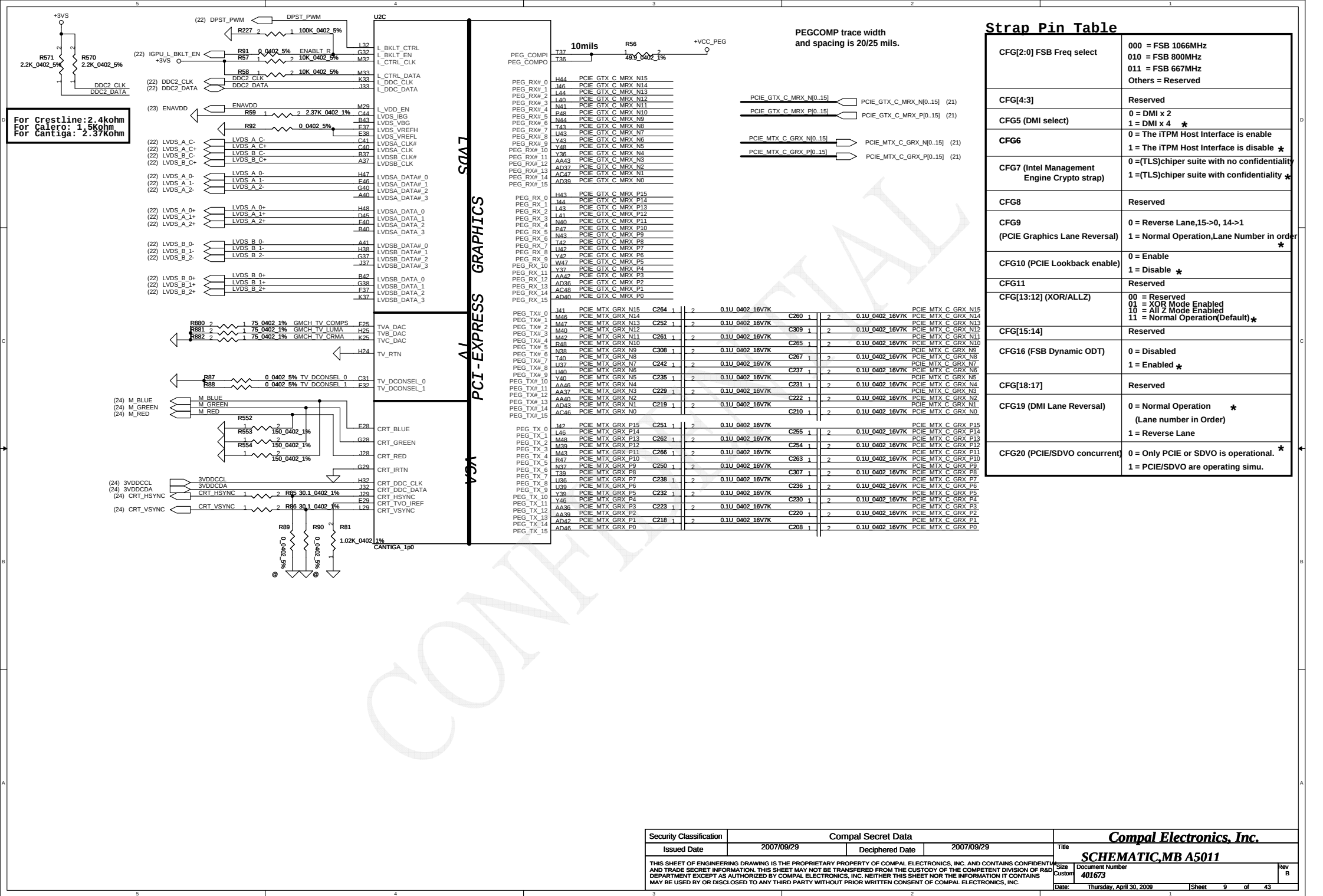


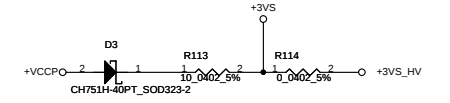
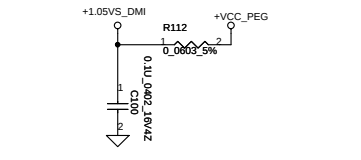
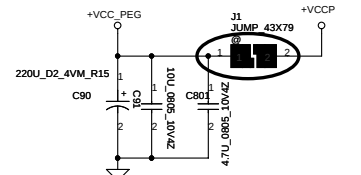
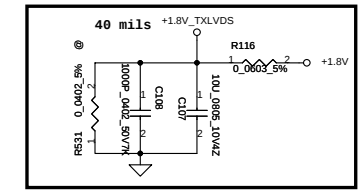
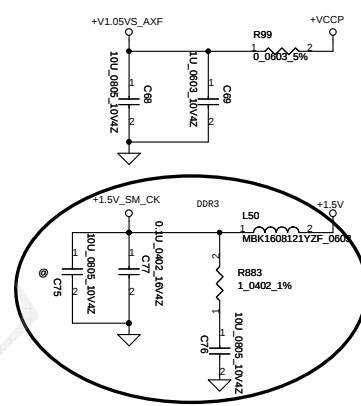
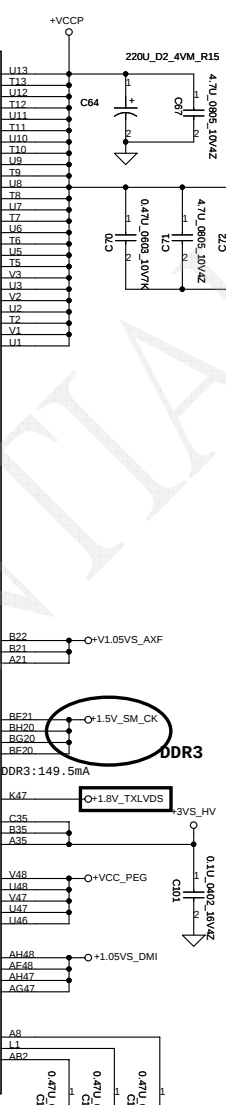
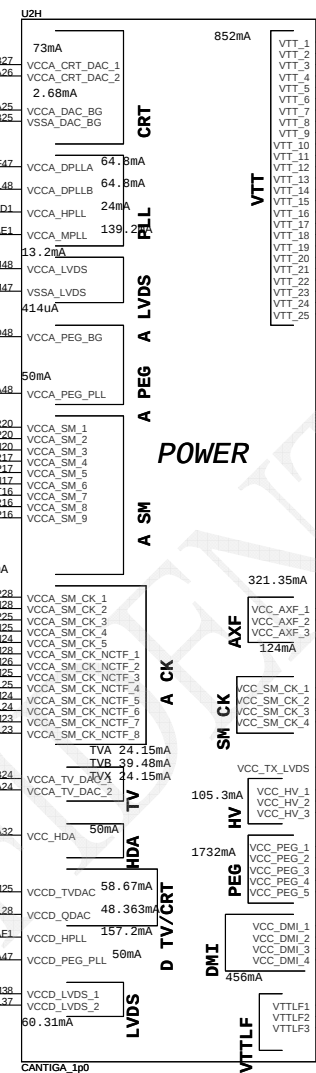
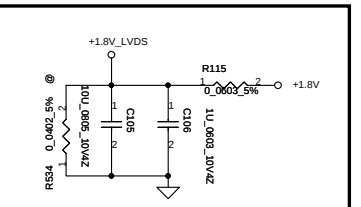
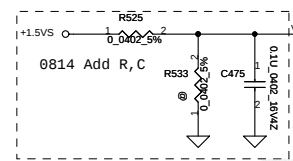
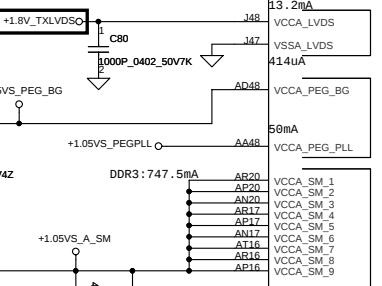
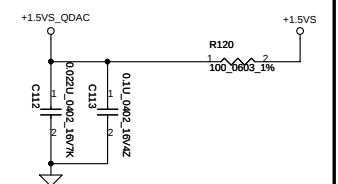
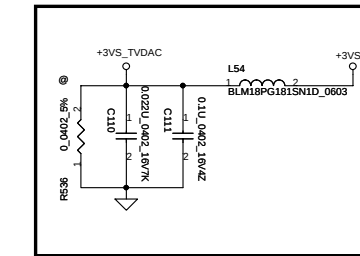
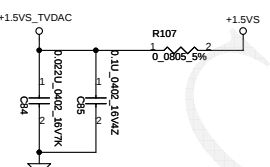
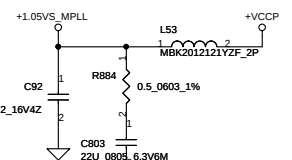
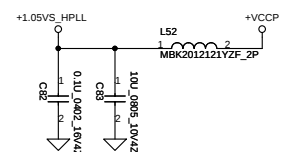
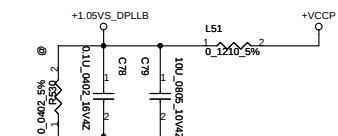
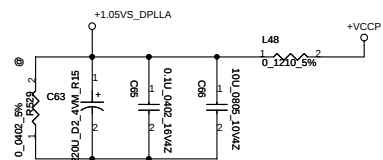
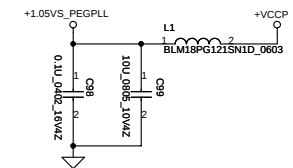
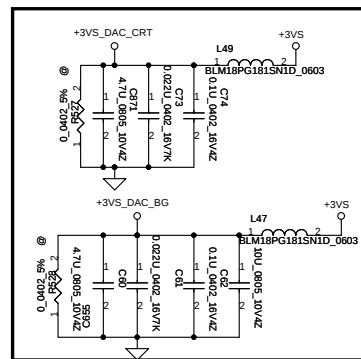
DDR_B_DQS#[0..7] (14)



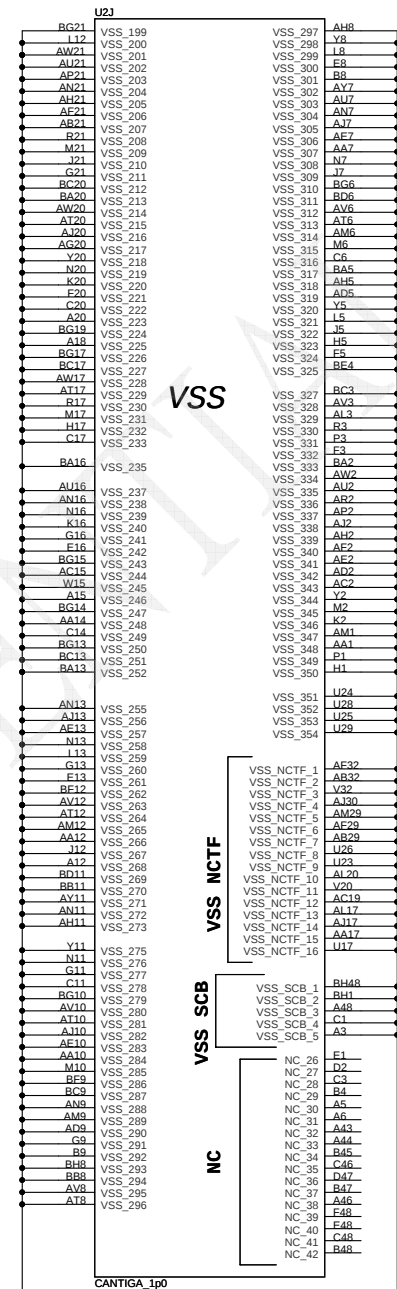
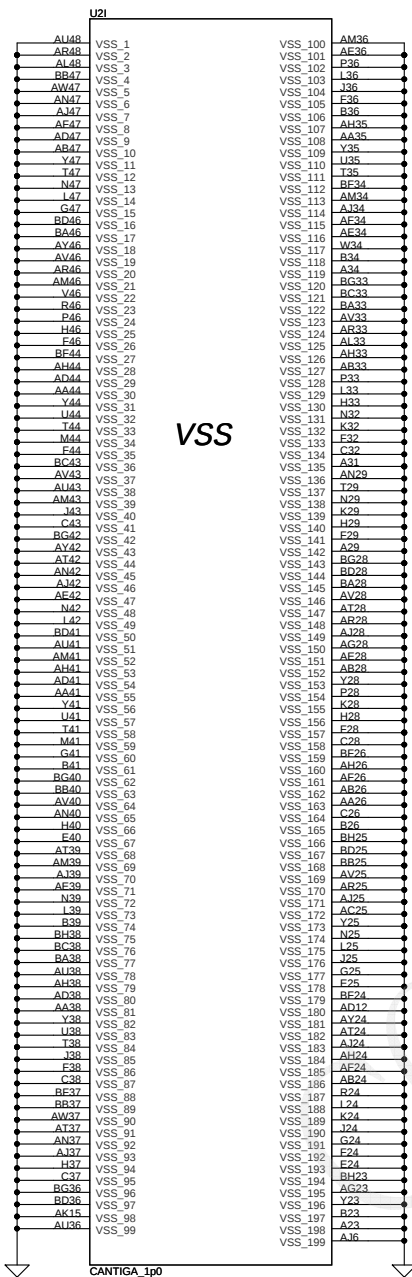
DDR_B_MA[0..14] (14)

Security Classification		Compal Secret Data		Title	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Document Number	SCHEMATIC, MB A5011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev B	401673
Date: Thursday, April 30, 2009				Sheet	8 of 43

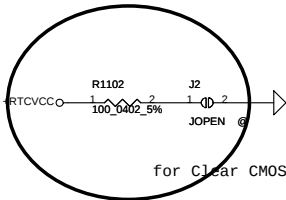
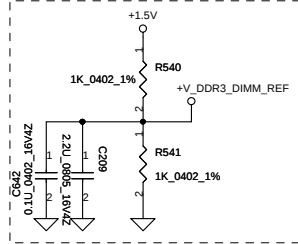




Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Title	SCHEMATIC, MB A5011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSMITTED TO THE CUSTODY OF THE COMPETENT DIVISION OF THE DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401673
				Rev	B
				Date	Thursday, April 30, 2009
				Sheet	10 of 43

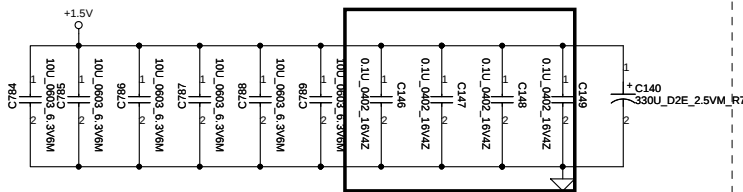


Security Classification	Compal Secret Data		Title	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	SCHEMATIC,MB A5011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev B
Date: Thursday, April 30, 2009				Sheet 12 of 43

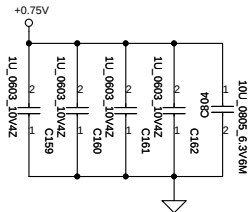


Layout Note:
Place near JP4

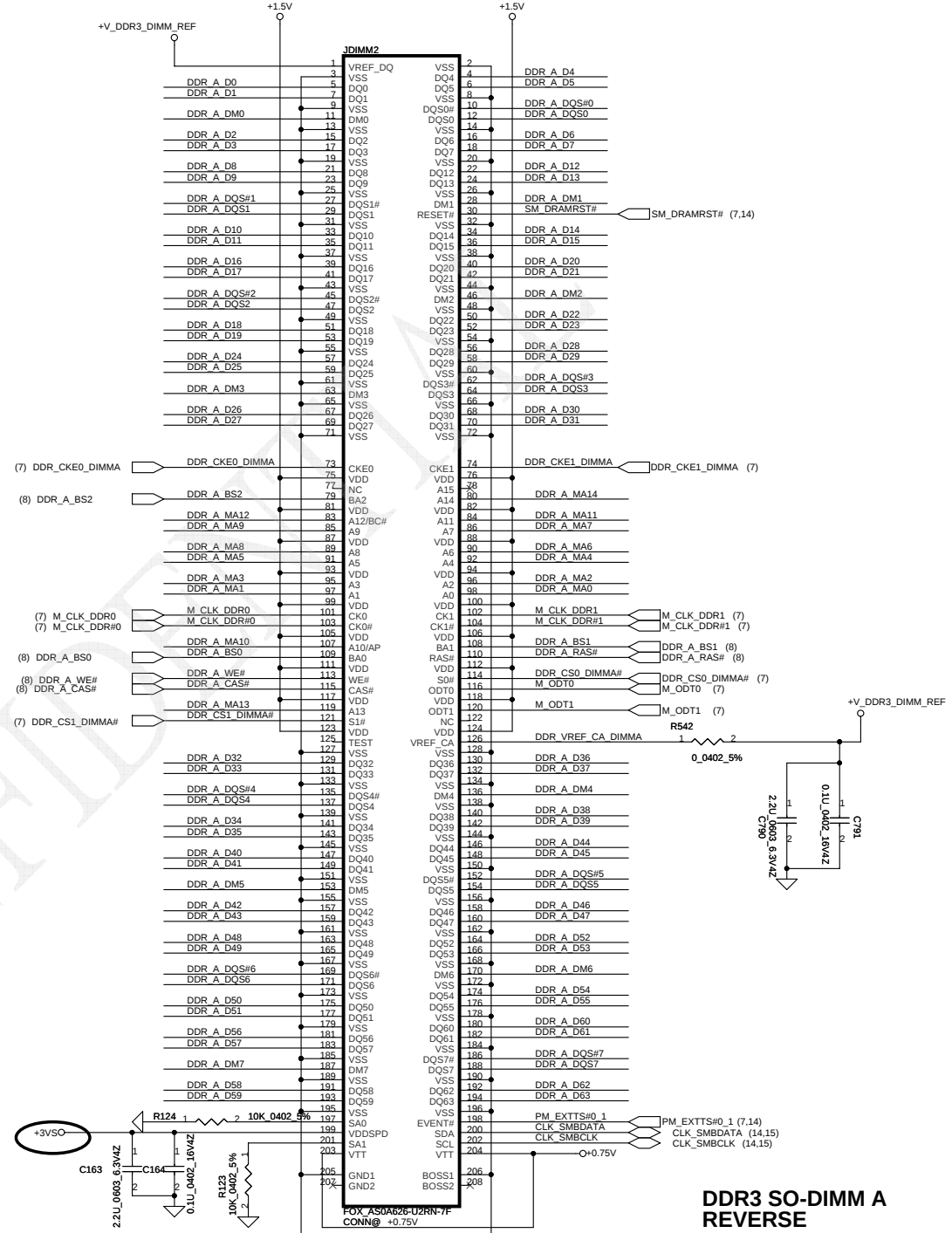
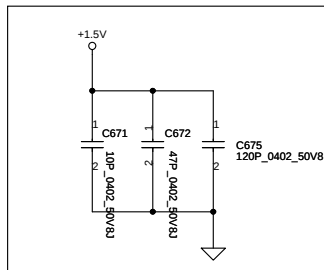
Layout Note: Place these 4 Caps near Command
and Control signals of DIMM



Layout Note:
Place near JP4.203 & JP4.204

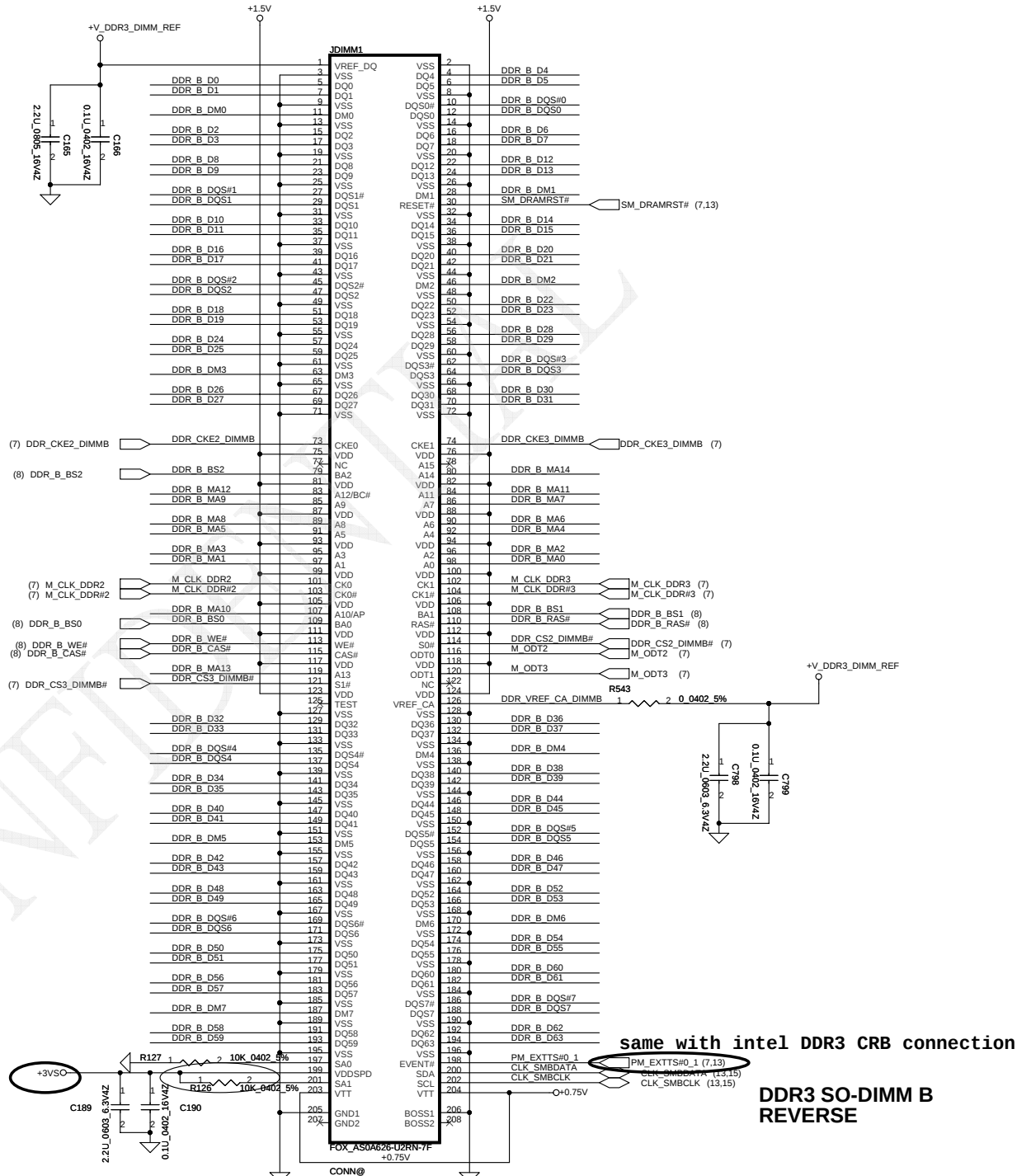
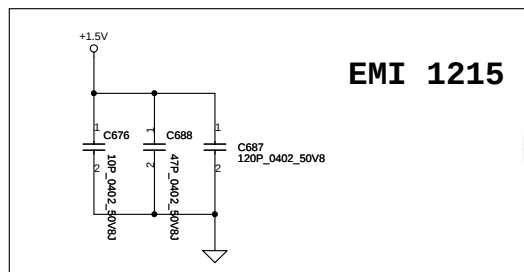
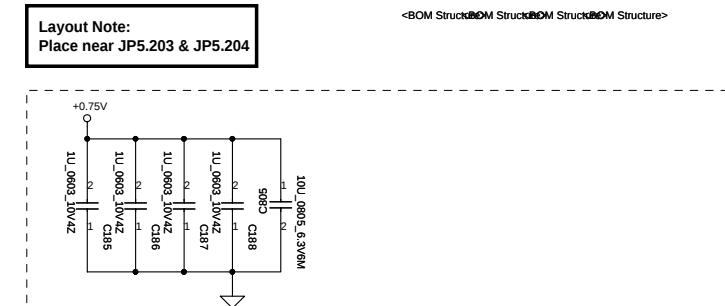
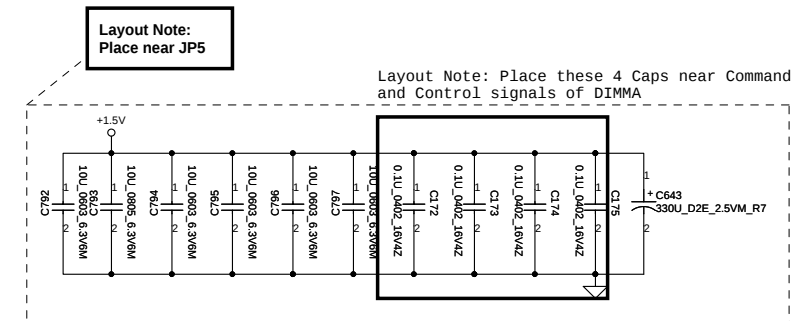
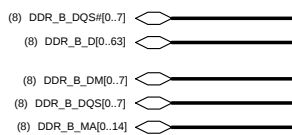


EMI 1215



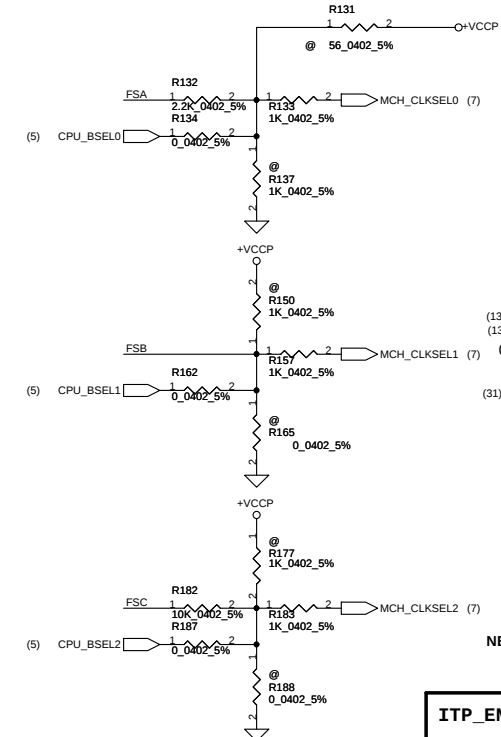
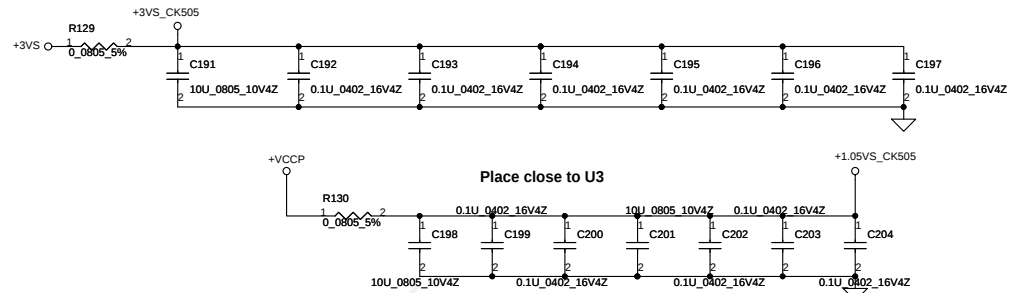
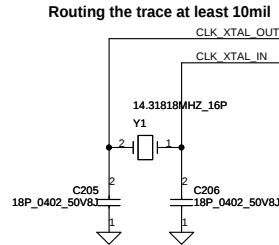
DDR3 SO-DIMM A
REVERSE

Security Classification		Compal Secret Data				Compal Electronics, Inc.					
Issued Date		2007/09/29		Deciphered Date		2007/09/29		Title			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						SCHEMATIC, MB A5011					
						Doc No		Document Number		Rev	
						401673		401673		B	
						Date		Thursday, April 30, 2009		Sheet 13 of 43	

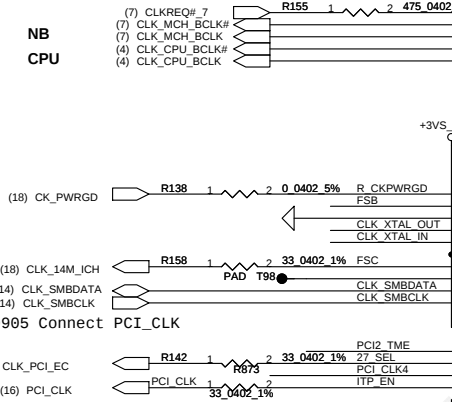


Security Classification		Compal Secret Data		Compal Electronics, Inc. SCHEMATIC, MB A5011	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev B
				401673	
				Date: Thursday, April 30, 2009	Sheet 14 of 43

FSC	FSB	FSA	CPU	SRC	PCI	REF	DOT_96	USB
CLKSEL2	CLKSEL1	CLKSEL0	MHZ	MHZ	MHZ	MHZ	MHZ	MHZ
0	0	0	266	100	33.3	14.318	96.0	48.0
0	0	1	133	100	33.3	14.318	96.0	48.0
0	1	0	200	100	33.3	14.318	96.0	48.0
0	1	1	166	100	33.3	14.318	96.0	48.0
1	0	0	333	100	33.3	14.318	96.0	48.0
1	0	1	100	100	33.3	14.318	96.0	48.0
1	1	0	400	100	33.3	14.318	96.0	48.0
1	1	1	Reserved					



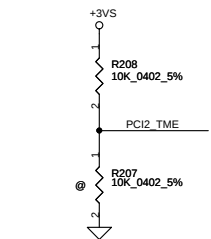
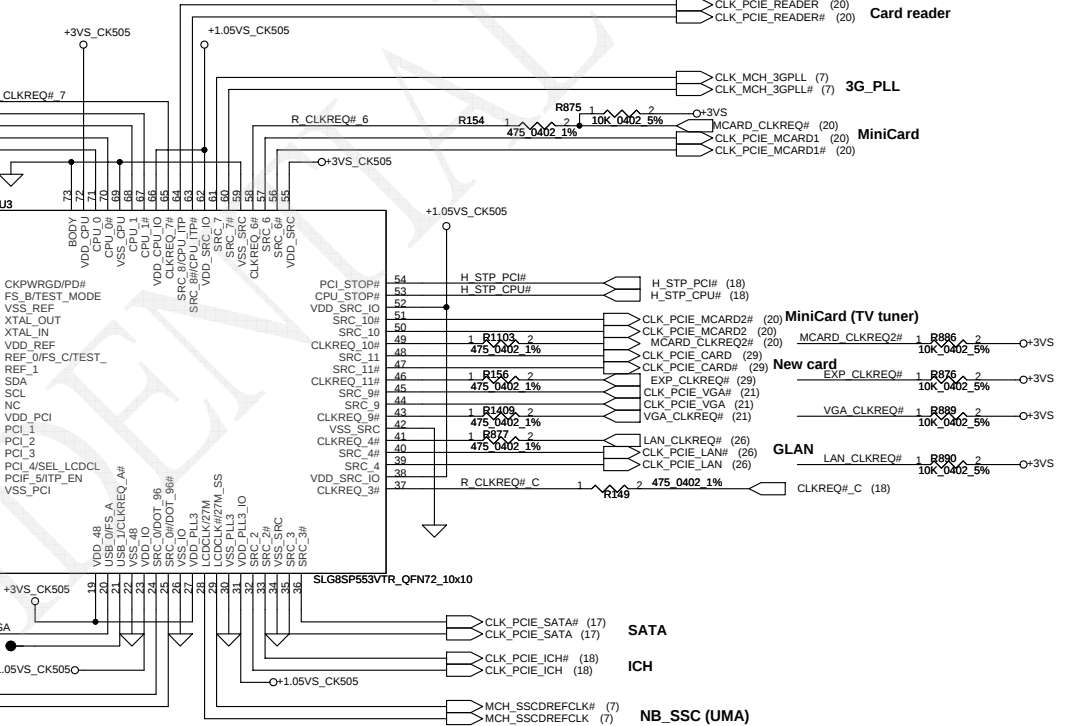
NB
CPU



NB (UMA)

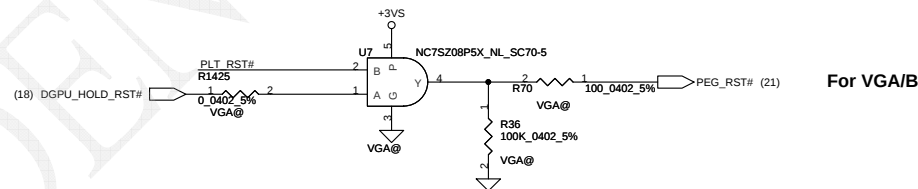
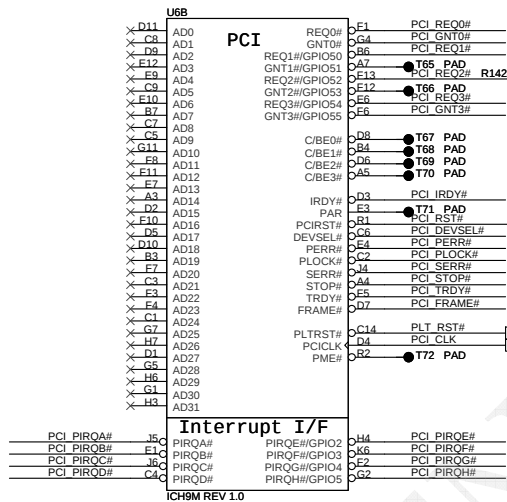
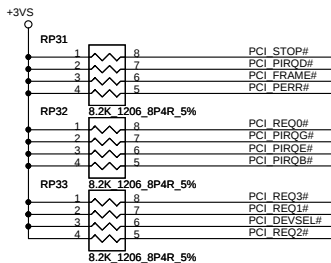
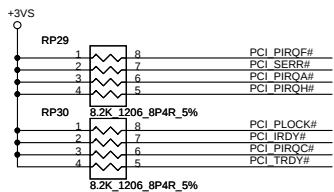
(7) CLK_MCH_DREFCLK#
(7) CLK_MCH_DREFCLK#

ITP_EN	0 = SRC8/SRC8# 1 = ITP/ITP#
PCI_CLK3	0 = Enable DOT96 & SRC1(UMA) 1 = Enable SRC0 & 27MHz(DIS)

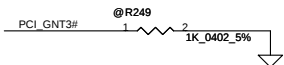


for ICS Overclocking setting
0=Overclocking of CPU and SRC allowed
1=Overclocking of CPU and SRC NOT allowed

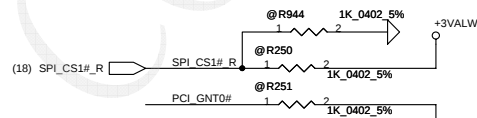
Security Classification		Compal Secret Data		Title	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev B
				401673	
Date: Thursday, April 30, 2009				Sheet 15	of 43

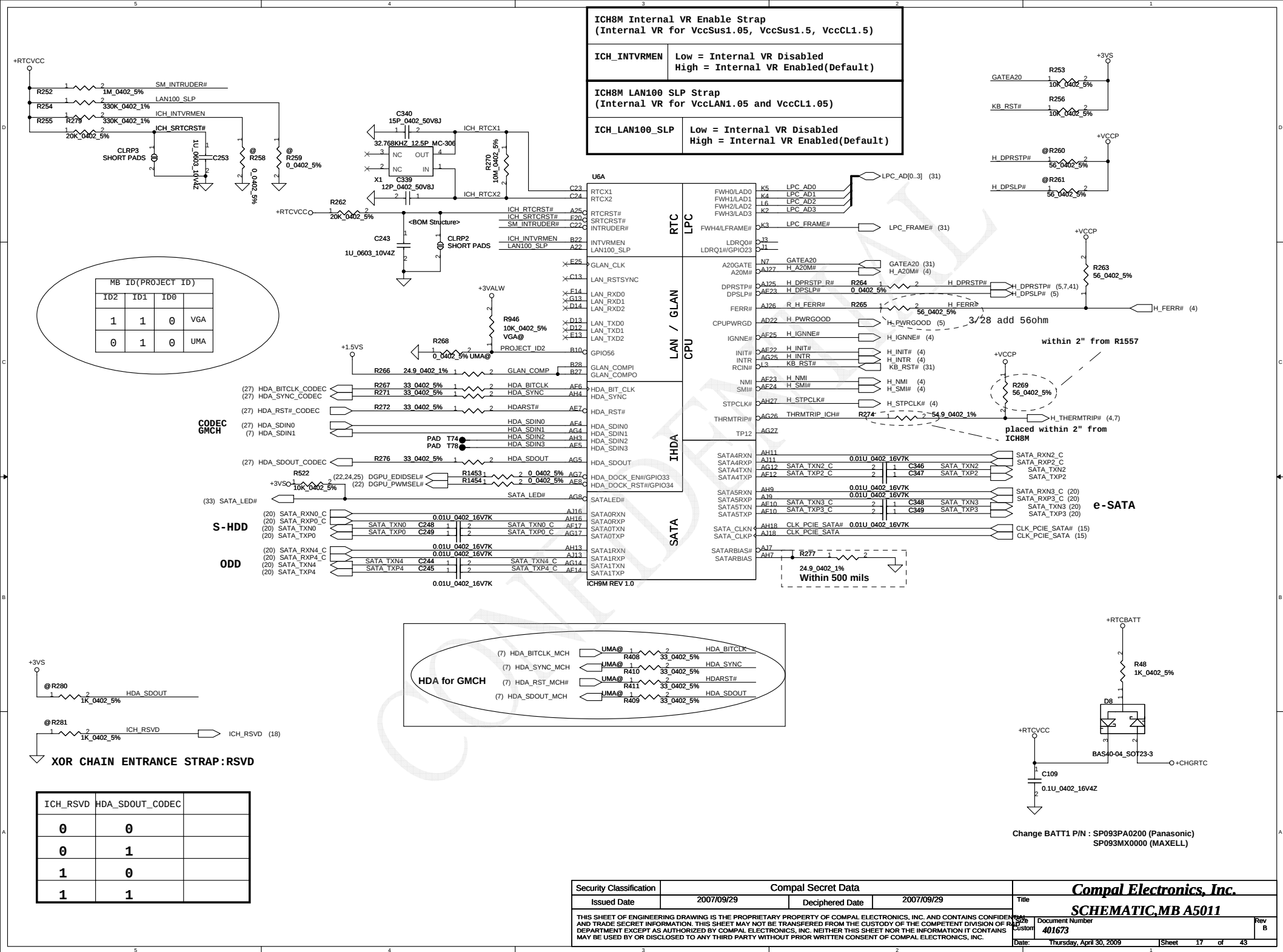


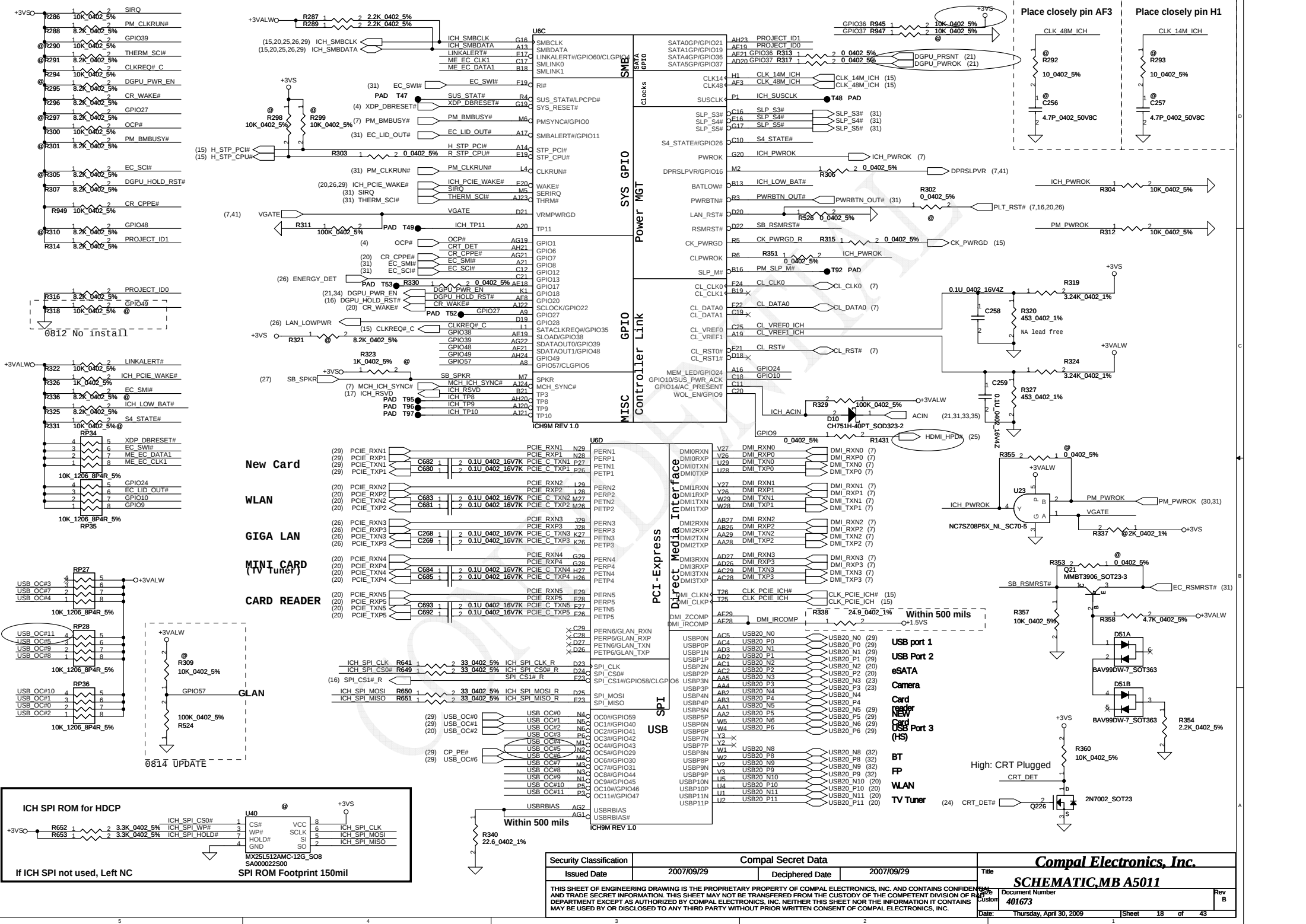
A16 swap override Strap	
PCI_GNT3#	Low= A16 swap override Enble High= Default *



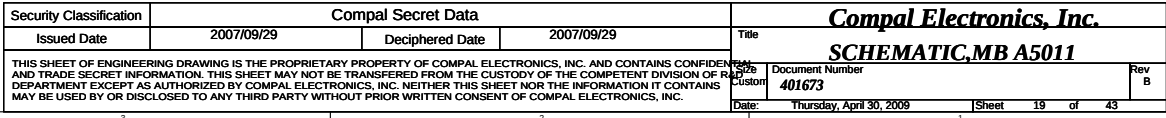
Boot BIOS Strap		
PCI_GNT0#	SPI_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC *

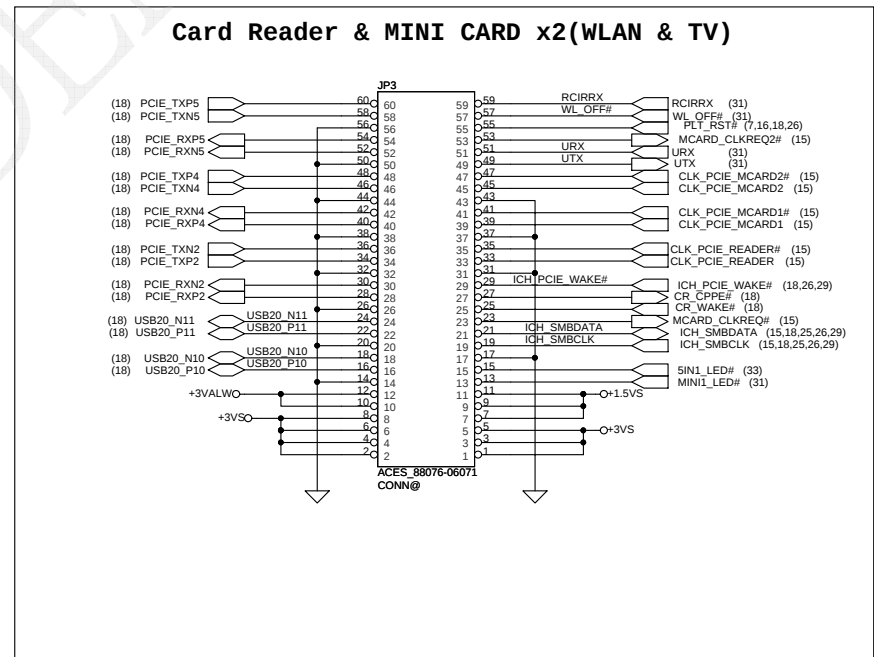
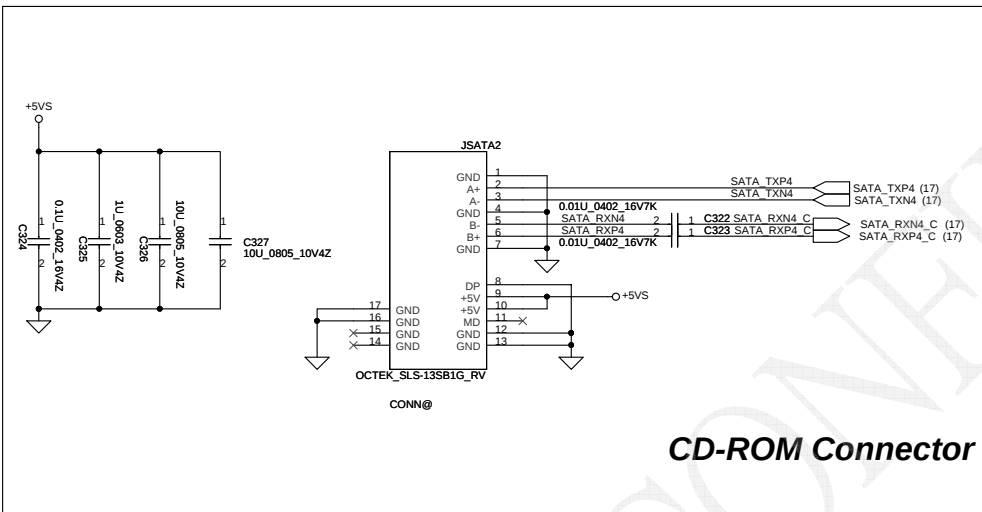
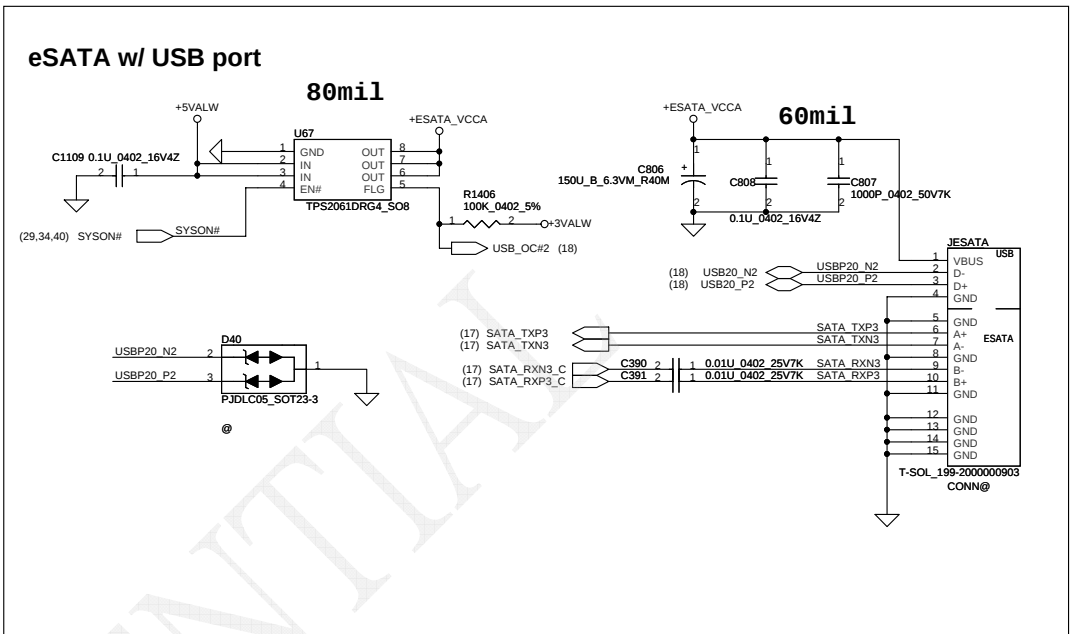
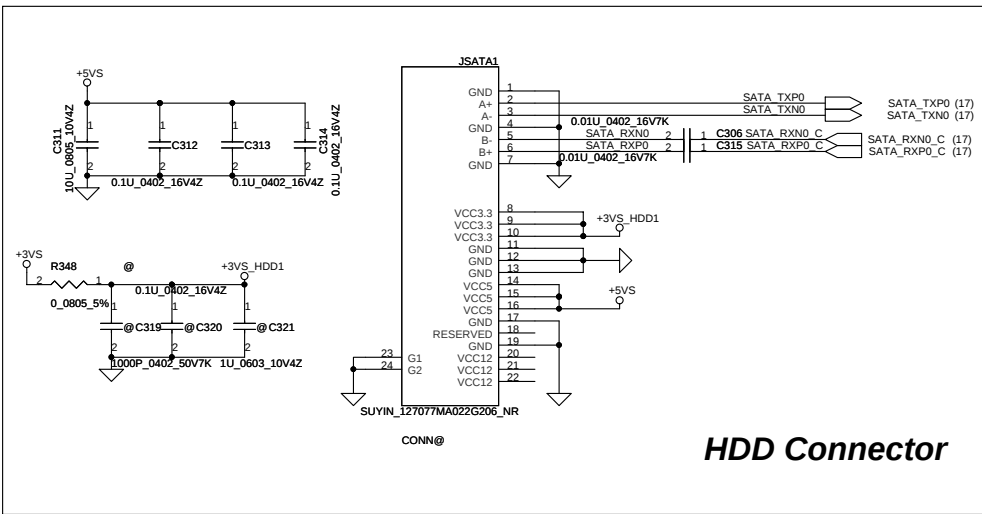




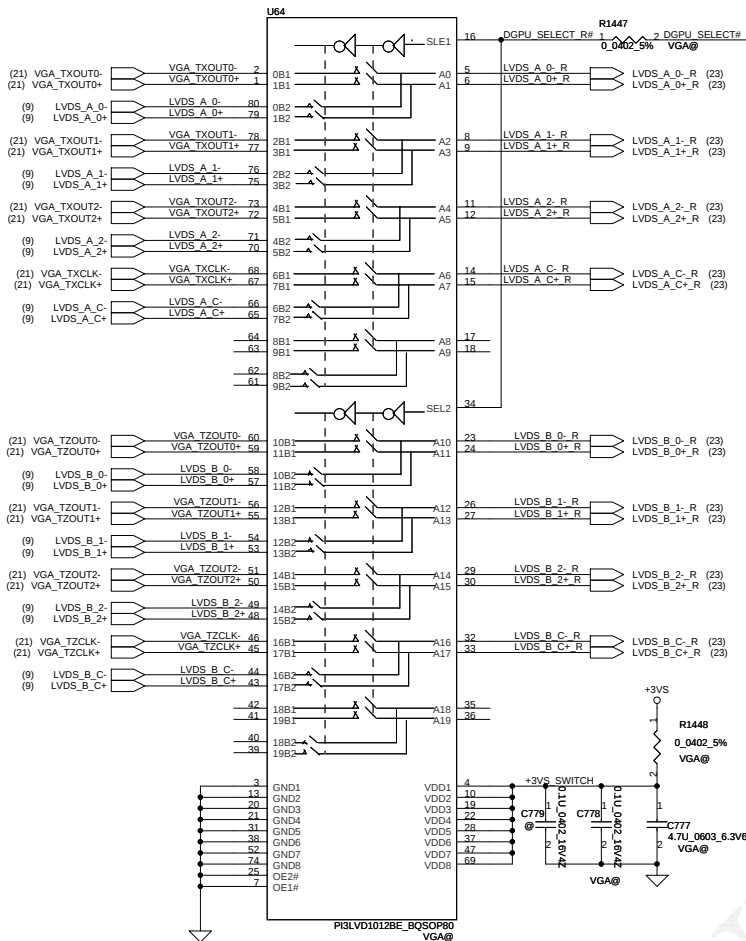


Security Classification		Compal Secret Data	
Issued Date	2007/09/29	Deciphered Date	2007/09/29
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPANY OR ANY OF ITS EMPLOYEES OR CONTRACTORS WITHOUT THE WRITTEN CONSENT OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			
Title		Compal Electronics, Inc.	
Document Number		SCHEMATIC, MB A5011	
Date		Thursday, April 30, 2009	
Sheet		18 of 43	

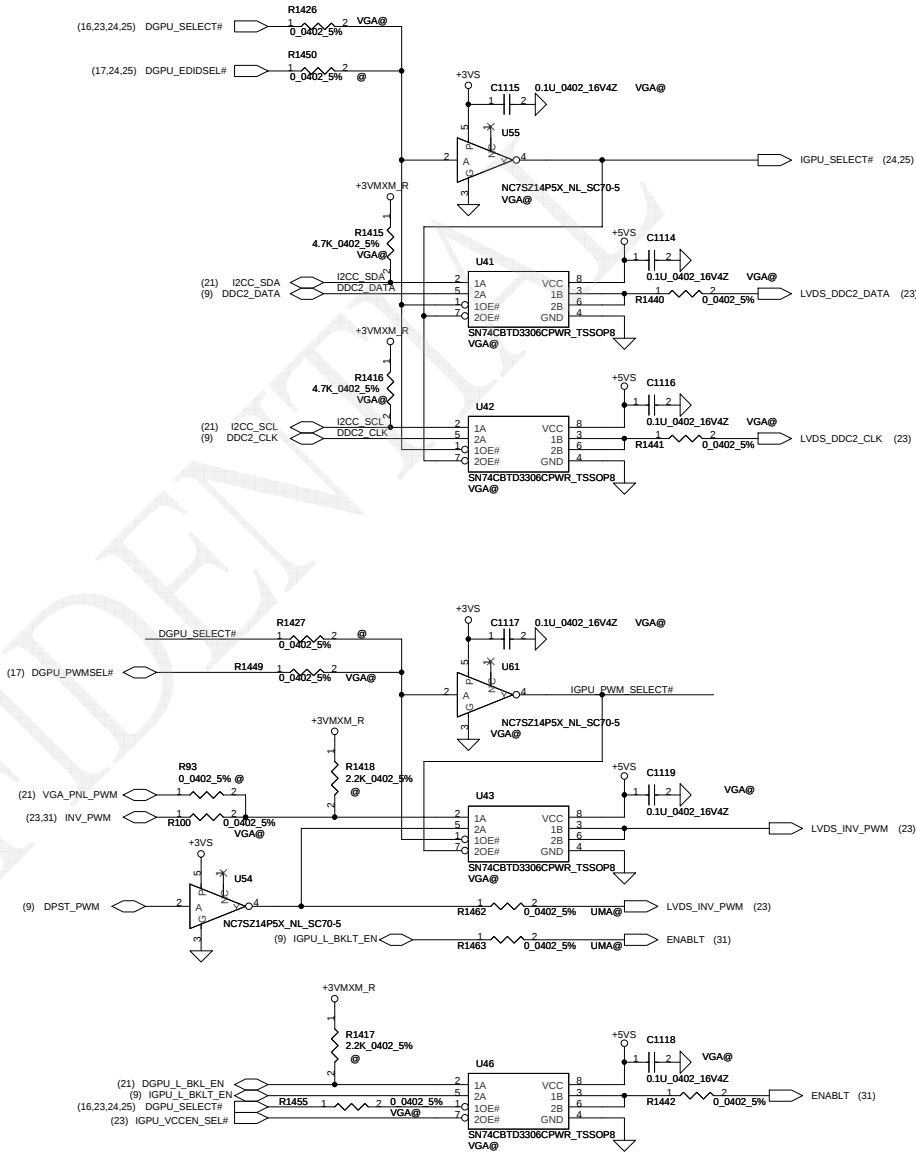
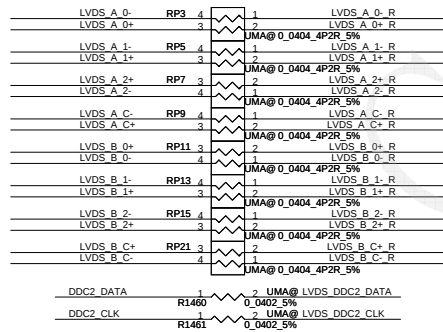




Security Classification		Compal Secret Data				Compal Electronics, Inc.					
Issued Date		2007/09/29		Deciphered Date		2007/09/29		Title			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						SCHEMATIC, MB A5011					
						Doc Number		401673		Rev B	
						Date		Thursday, April 30, 2009		Sheet 20 of 43	

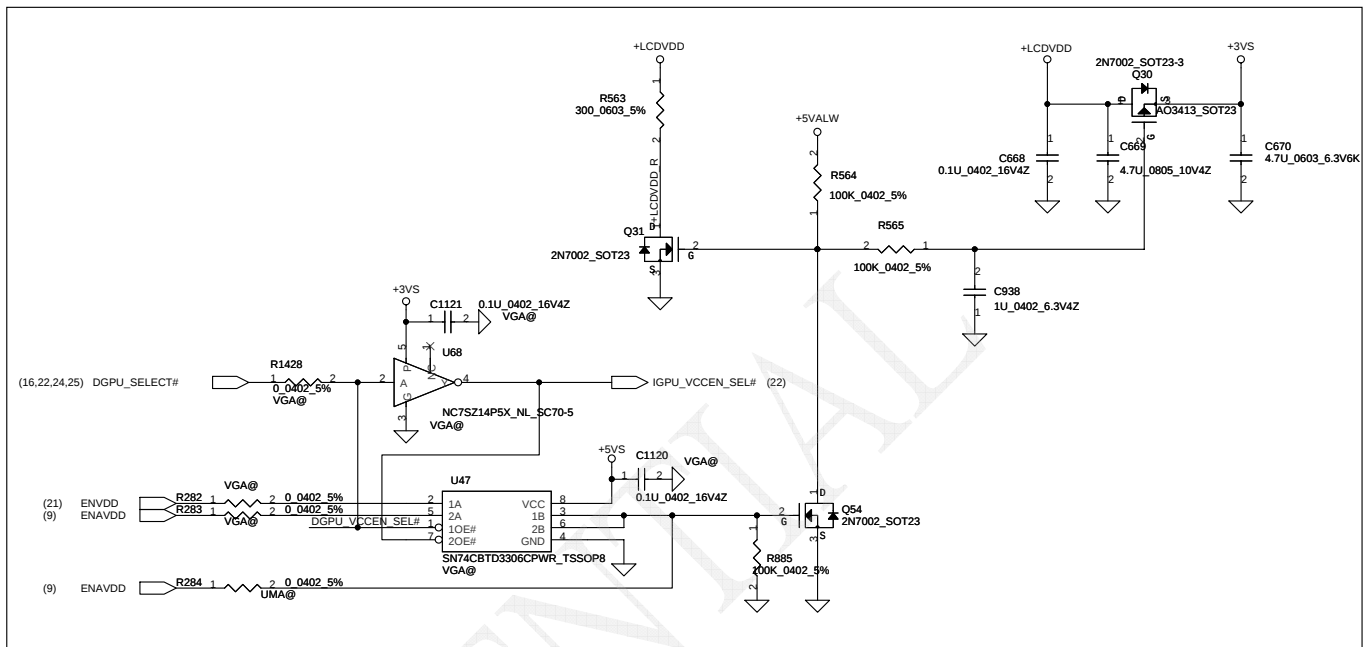
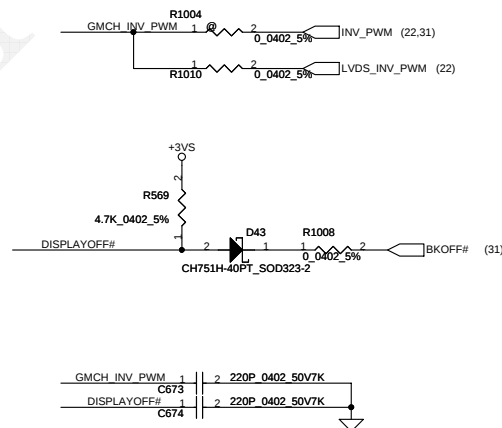
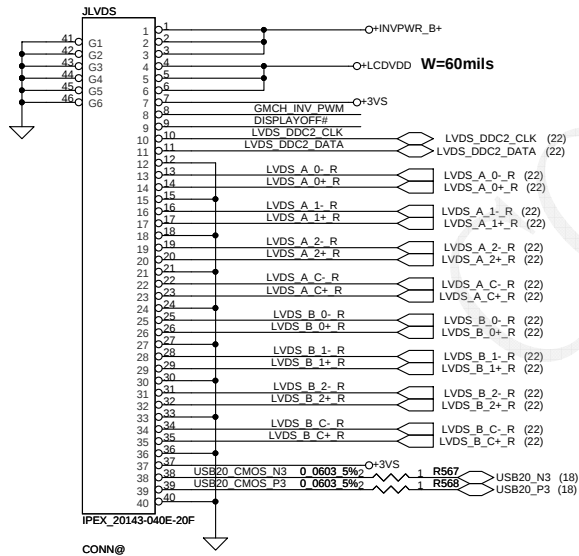
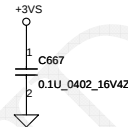
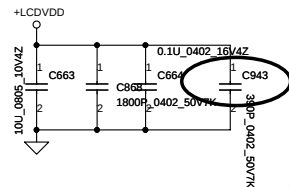
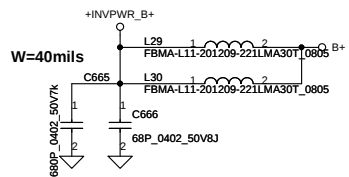


UMA ONLY



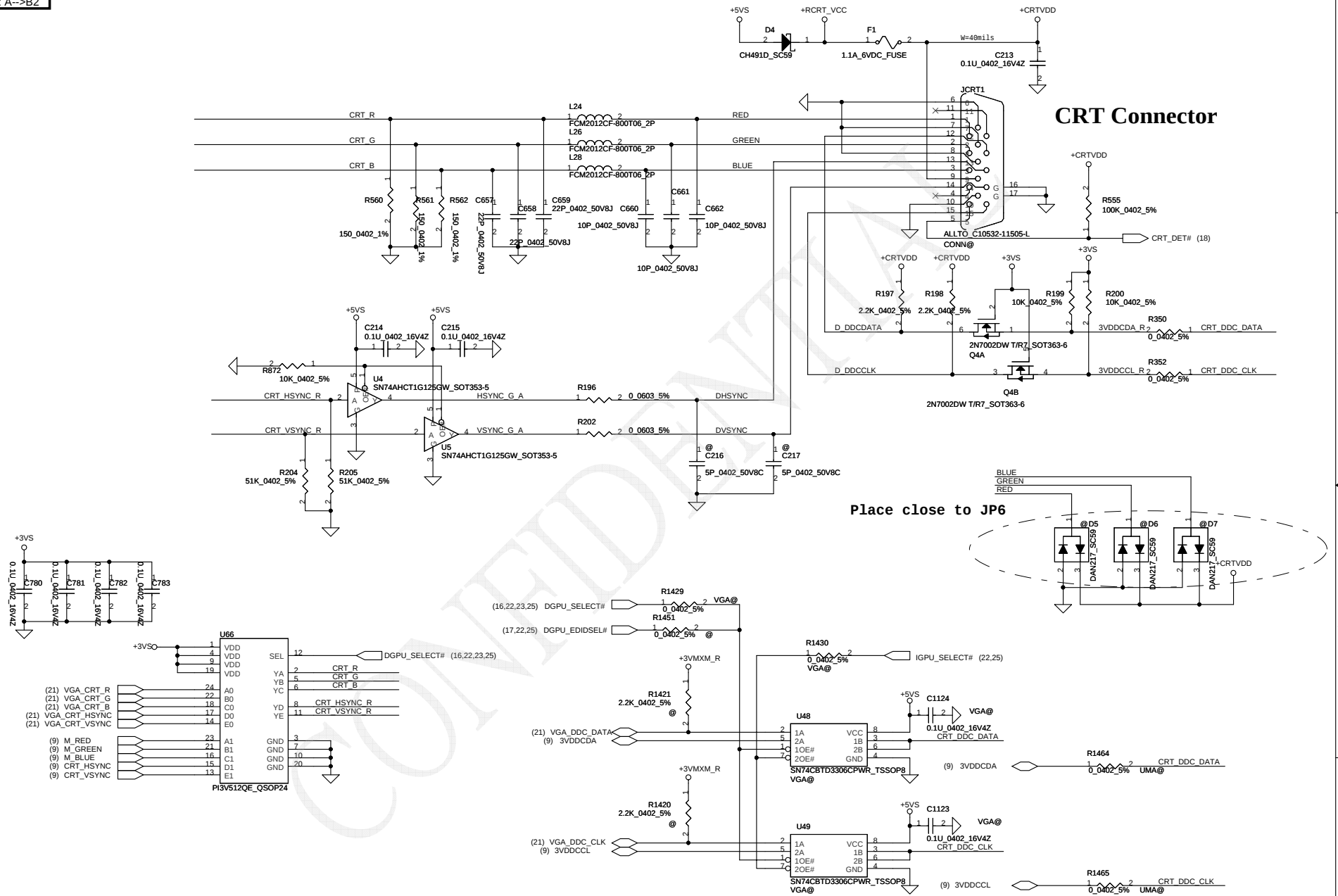
Security Classification		Compal Secret Data		Title	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	401673
				Rev	8
				Date	Thursday, April 30, 2009
				Sheet	22 of 43

LVDS and USB CAM connector

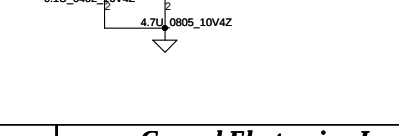
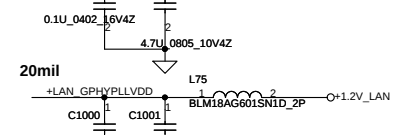
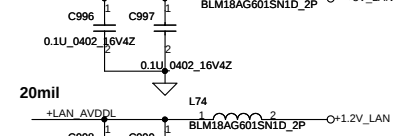
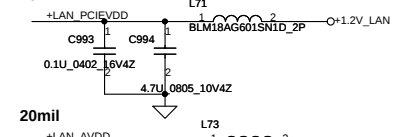
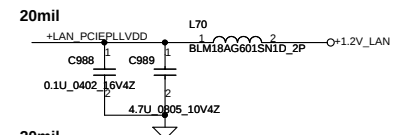
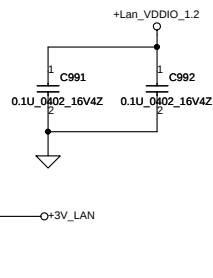
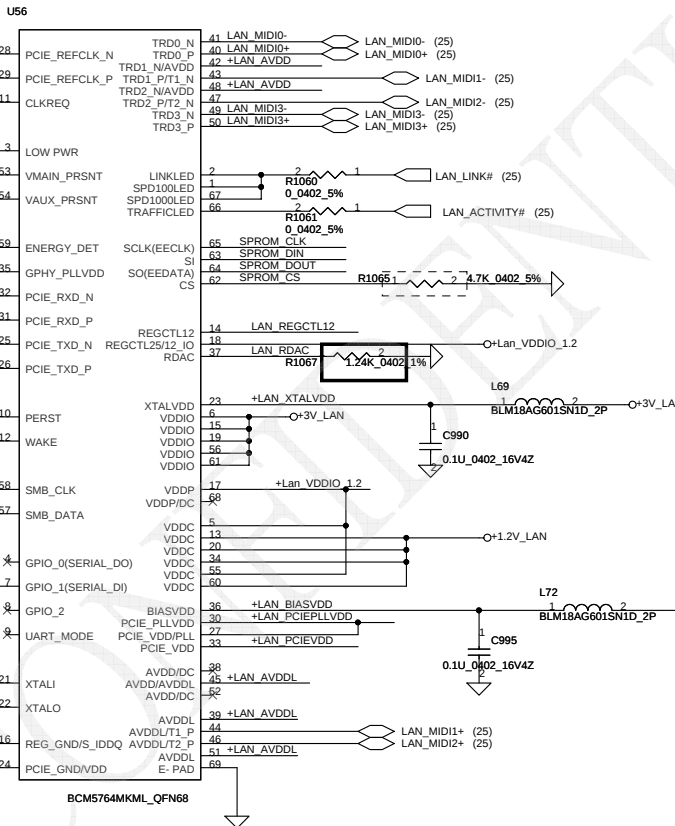
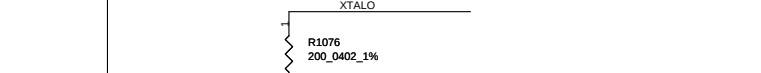
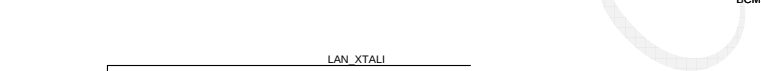
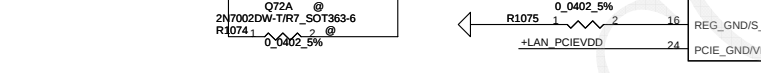
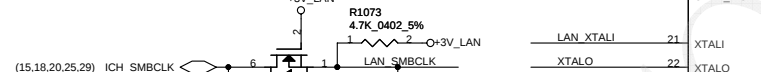
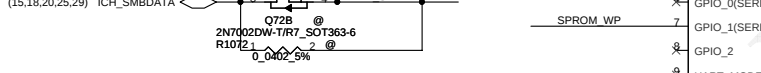
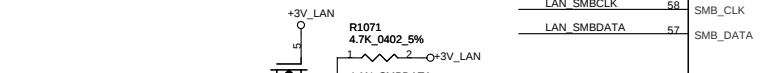
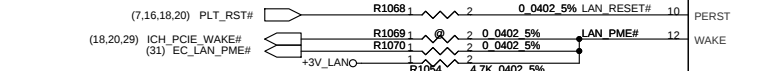
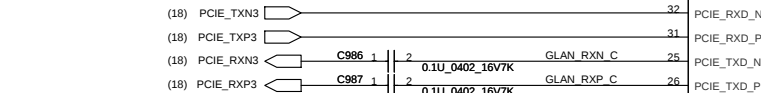
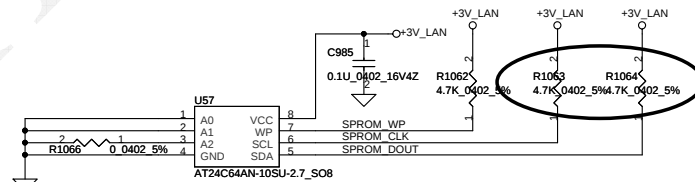
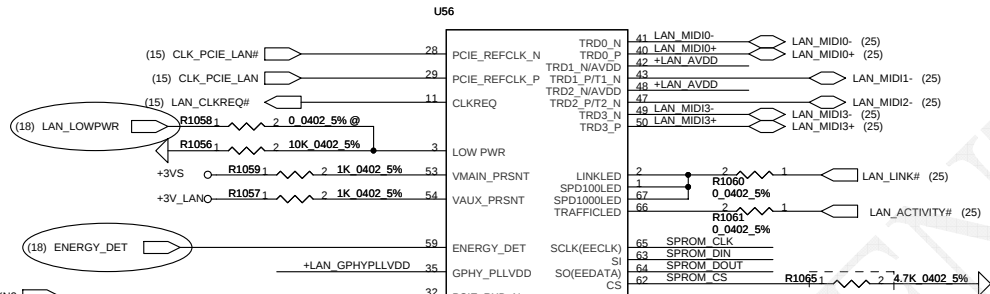
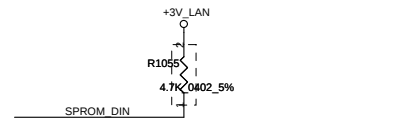
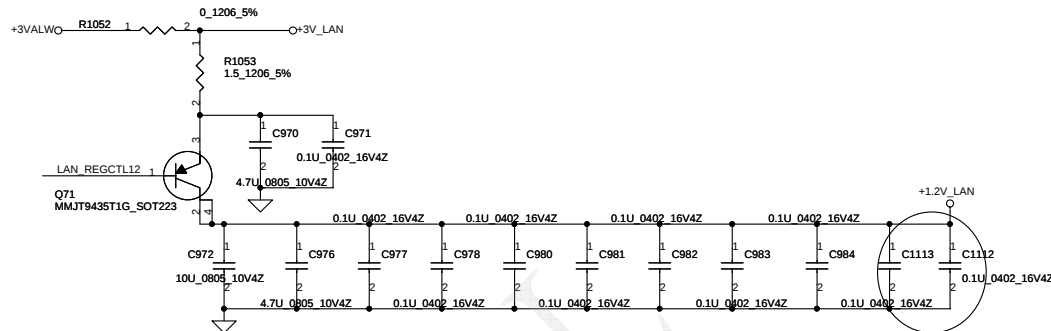
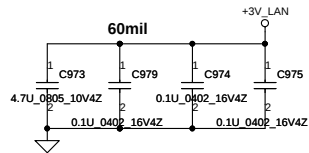


Security Classification	Compal Secret Data			Title	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev B
				401673	
				Date: Thursday, April 30, 2009	Sheet 23 of 43

NOTE: L : A $\rightarrow$ B1
H: A $\rightarrow$ B2

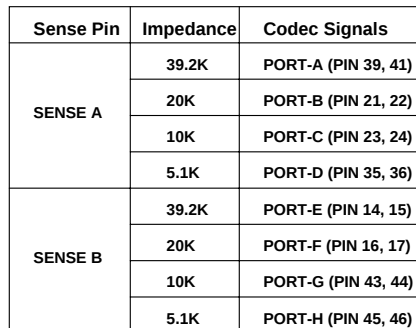
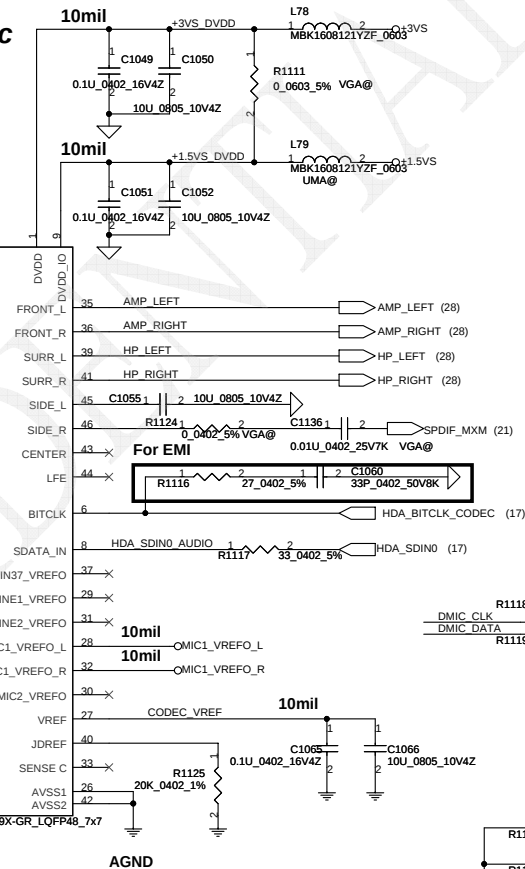


Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Title	SCHEMATIC, MB A5011	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Doc No	Document Number	Rev B
					401673	
Date:				Thursday, April 30, 2009	Sheet	24 of 43



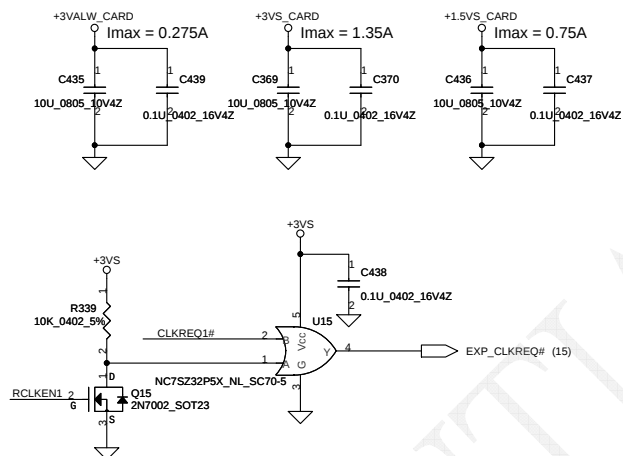
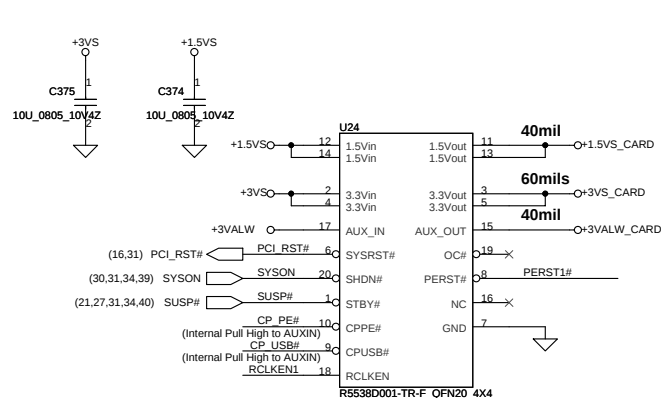
Place closed to Pin2 & Pin59

Security Classification		Compal Secret Data		Title	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	SCHEMATIC,MB A5011	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev B	401673
Date:	Thursday, April 30, 2009	Sheet	26	of	43

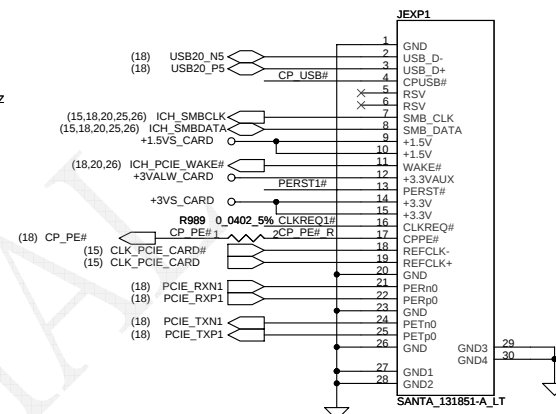


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/09/20	Deciphered Date	2008/09/20	Title	Schematic, MB A5011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev B
				401673	
Date:		Thursday, April 30, 2009		Sheet	27 of 43

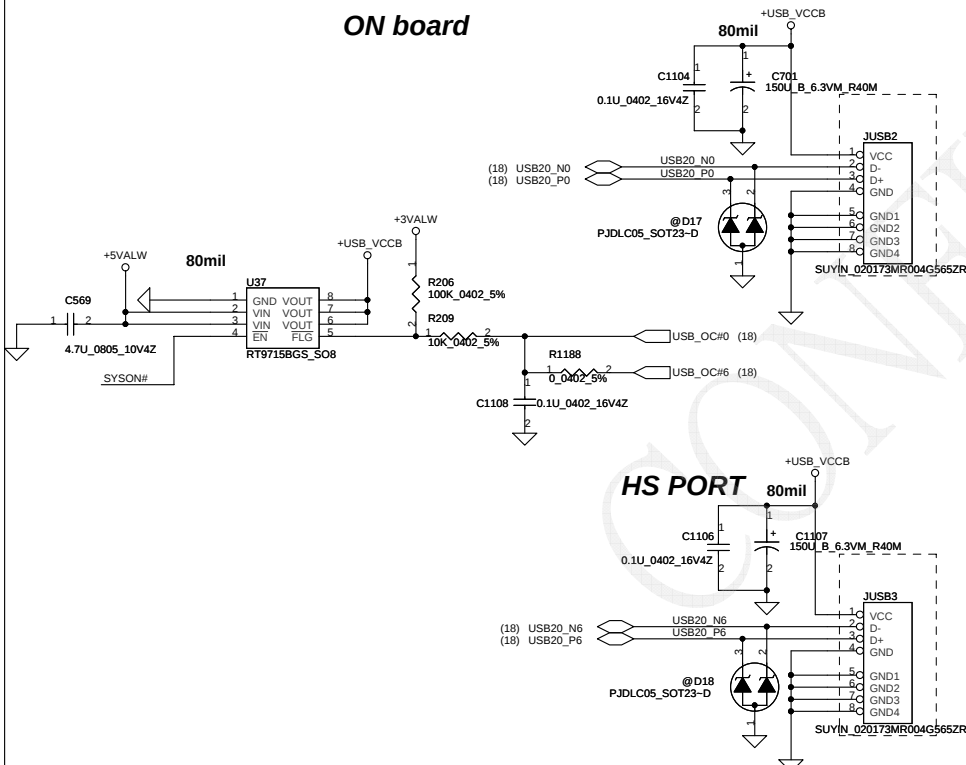
New Card Power Switch



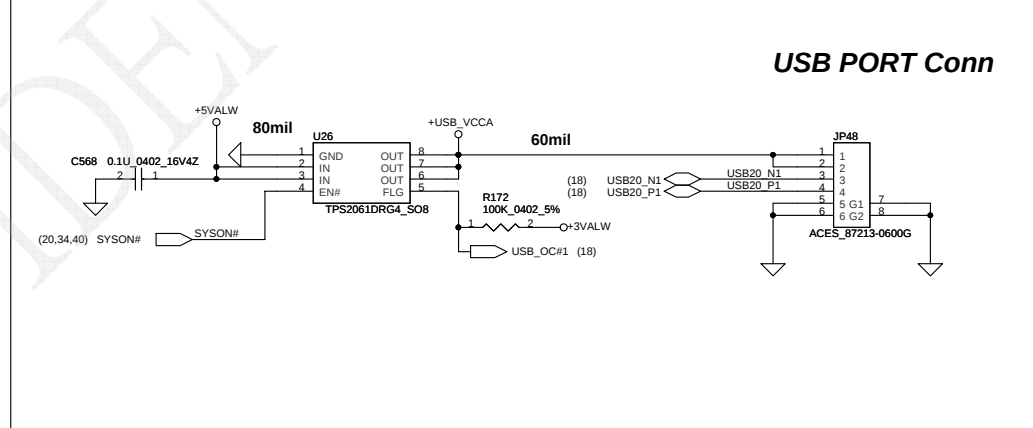
New Card Socket (Left/TOP)



ON board

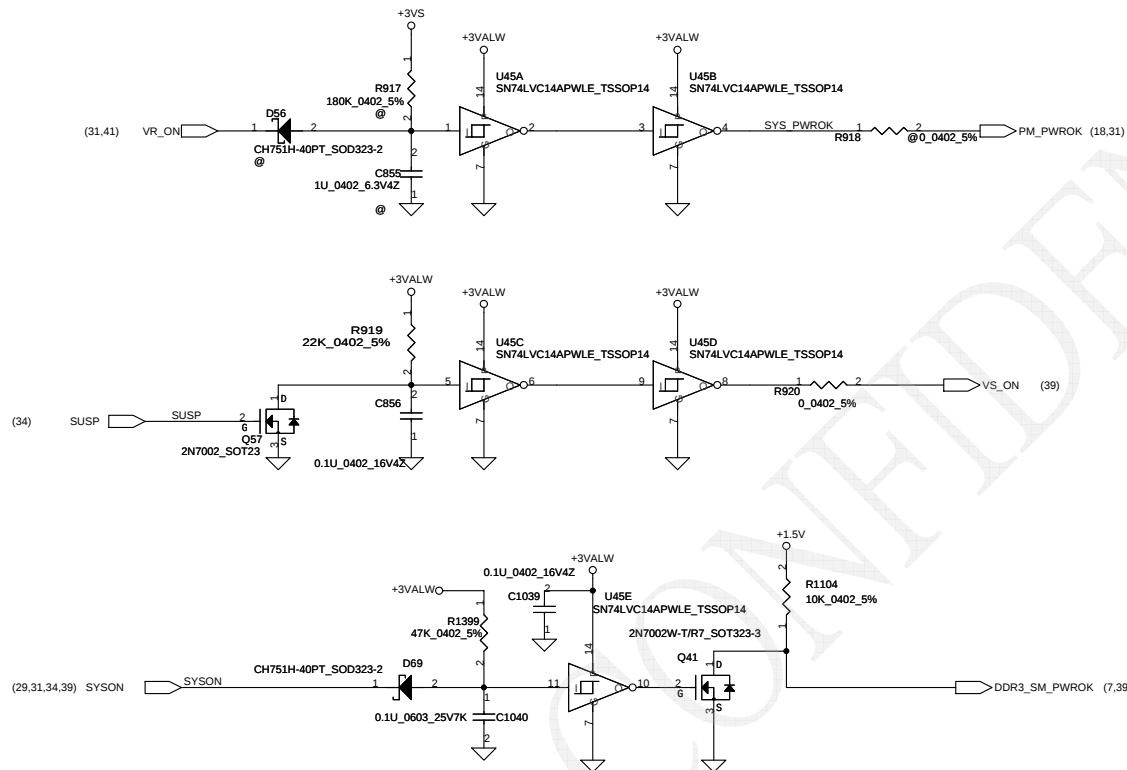
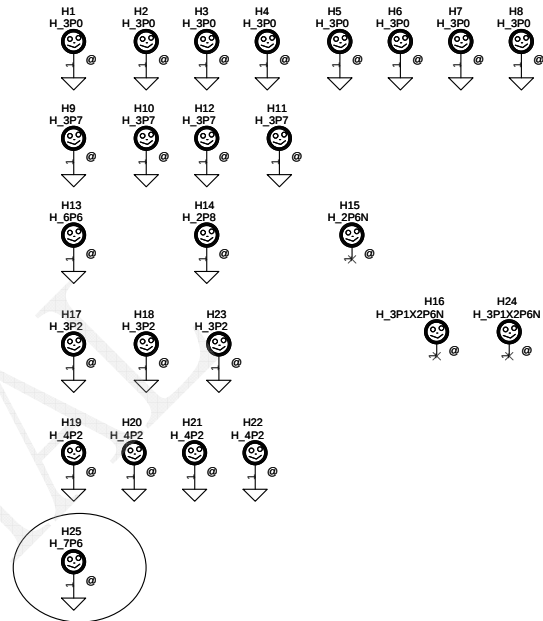
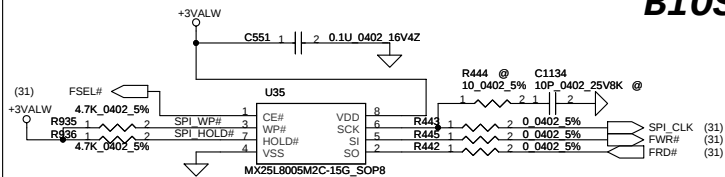


USB PORT Conn

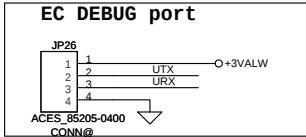


Security Classification		Compal Secret Data		Compal Electronics, Inc. SCHEMATIC, MB A5011	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Docuement Number	Rev B
				401673	
				Date: Thursday, April 30, 2009	Sheet 29 of 43

BIOS



Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2007/09/29				Title			
Deciphered Date				2007/09/29				SCHEMATIC, MB A5011			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number				Rev B			
Date:				Thursday, April 30, 2009				Sheet 30 of 43			



VCC	3.3V+/-5%				
Ra	100K+/-5%				
Board ID	Rb	V _{AD_BID} min	V _{AD_BID} typ	V _{AD_BID} max	
0		0V	0V	0.155V	
1	8.2K+/-5%	0.168V	0.250V	0.362V	
* 2	18K+/-5%	0.375V	0.503V	0.621V	
3	33K+/-5%	0.634V	0.819V	0.945V	
4	56K+/-5%	0.958V	1.185V	1.359V	
5	100K+/-5%	1.372V	1.650V	1.838V	
6	200K+/-5%	1.851V	2.200V	2.420V	
7	NC	2.433V	3.300V	3.300V	

TP CLK R462 1 2 4.7K 0402 5% +5VS

TP DATA R463 1 2 4.7K 0402 5%

F LPC ROM

The diagram shows a horizontal line representing the chip with pins 1, 2, and 13. Pin 1 is connected to +5VALWO. Pin 2 is connected to EC RCIRRX. Pin 13 is connected to RCIRRX (20). A resistor R213 (10K_0402_5%) is connected between pins 1 and 2. A diode D13 is connected between pins 2 and 13.

CH751H-40PT_S0P323-2

For EMI

CP1

KSO0 1 8
KSO1 2 7
KSO2 3 6
KSO3 4 5

100P, 1206, 8P4C, 50V8

CP2

KSIO 1 8
KSII 2 7
KSIZ 3 6

100P, 1206, 8P4C, 50V8

Figure 1: Pin connections of the 100P_1206_8P4C_50V8. The diagram shows two 8-pin packages. The left package is labeled "100P_1206_8P4C_50V8" and has pins KSO0 to KSO7. The right package is labeled "100P_1206_8P4C_50V8" and has pins KS04 to KS07. The left package has pins 26, 25, 24, 23, 22, 21, 20, 19, 18, 17, 16, 15, 14, 13, 12, 11, 10, 9, 8, 7, 6, 5, 4, 3, 2, 1. The right package has pins 1, 2, 3, 4, 5, 6, 7, 8. The diagram shows connections between the two packages: KSO0 to KS04, KSO1 to KS05, KSO2 to KS06, KSO3 to KS07. There are also connections between pins 26 and 28, 25 and 27, 24 and 26, 23 and 27, 22 and 26, 21 and 27, 20 and 26, 19 and 27, 18 and 26, 17 and 27, 16 and 26, 15 and 27, 14 and 26, 13 and 27, 12 and 26, 11 and 27, 10 and 26, 9 and 27, 8 and 26, 7 and 27, 6 and 26, 5 and 27, 4 and 26, 3 and 27, 2 and 26, 1 and 27.

P VIEW

100P_1206_8P4C_50V8

CP5

Pin	Label	Value
15	KS011	15
14	KS012	14
13	KS013	13
12	KS014	12
11	KS015	11
10	KS016	10
9	KS017	9
8	KS10	8
7	KS11	7
6	KS12	6
5	KS13	5
4	KS14	4
3	KS15	3
2	KS16	2
1	KS17	1

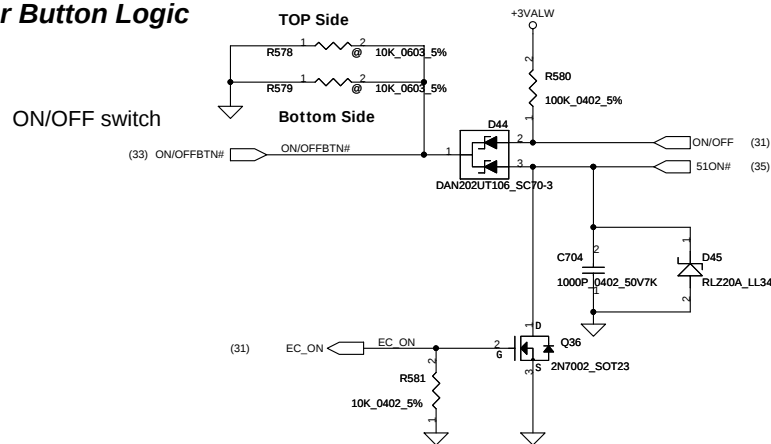
100P_1206_8P4C_50V8

CP6

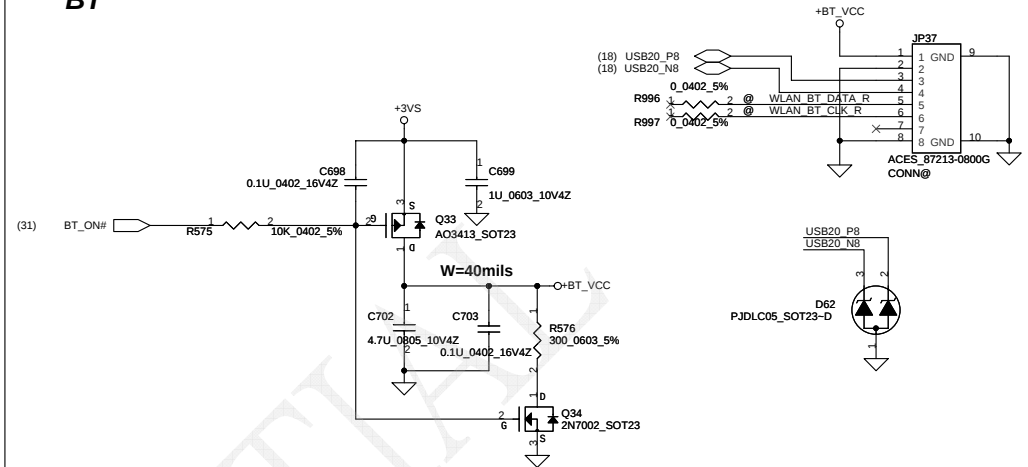
Pin	Label	Value
1	KS012	1
2	KS013	2
3	KS014	3
4	KS015	4

(Right)	ACES_88747-2601	100F-1200-8F-AC-SUVS	
	Compal Electronics, Inc. SCHEMATIC, MB A5011		
CONTAINS CONFIDENTIAL INFORMATION IF IT CONTAINS S, INC.	Document Number	401673	Rev
Date:	Thursday, April 30, 2009	Sheet	31 of 43

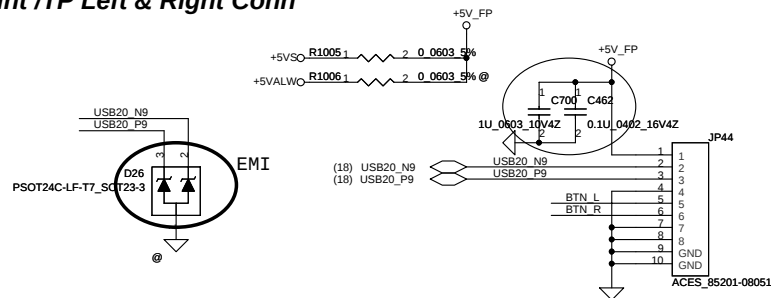
Power Button Logic



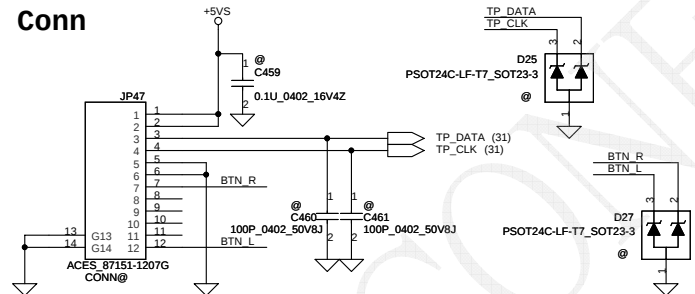
BT



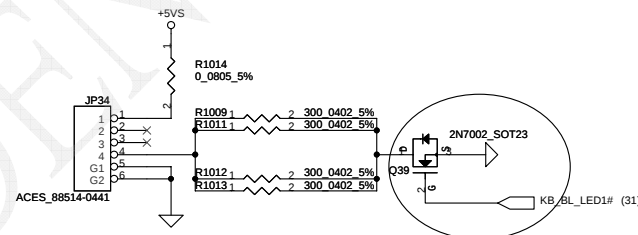
Finger Print /TP Left & Right Conn



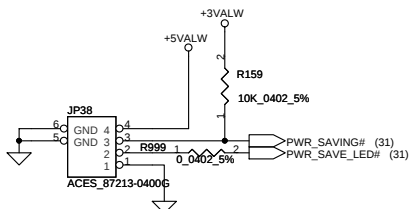
T/P Board Conn



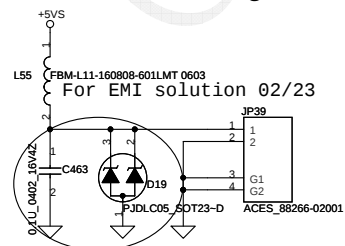
KB back light Conn



POWER SAVING Conn

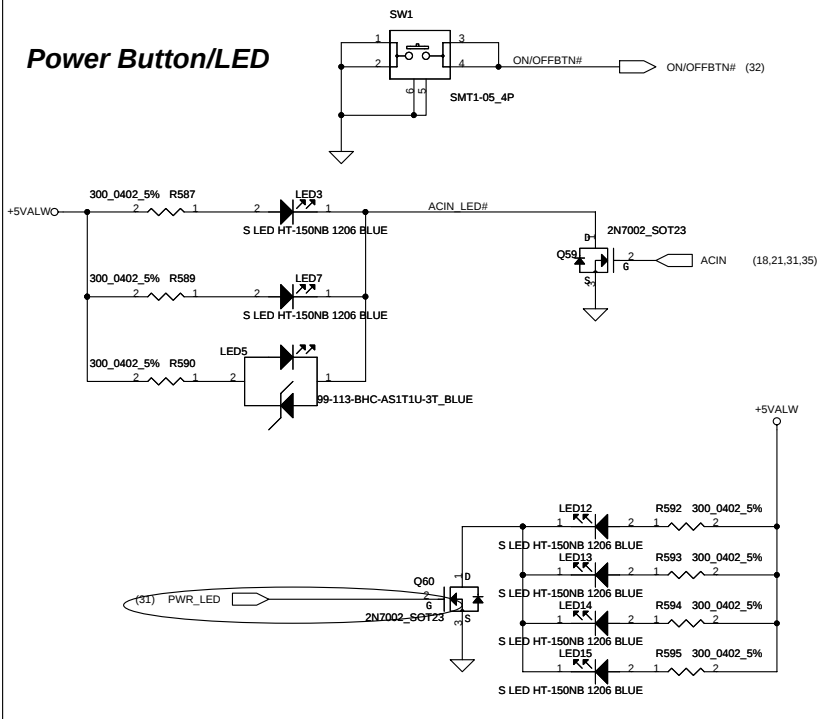


ACER LOGO BackLight Conn

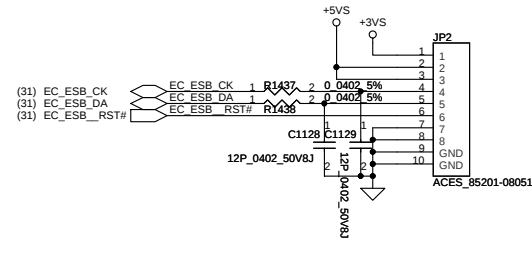


Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				Deciphered Date				Title			
2007/09/29				2007/09/29				SCHEMATIC, MB A5011			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number				Rev			
				401673				B			
Date:				Thursday, April 30, 2009				[Sheet 32 of 43]			

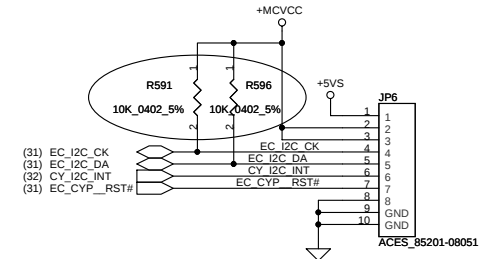
Power Button/LED



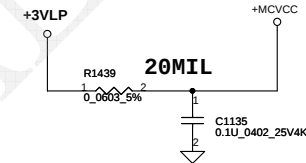
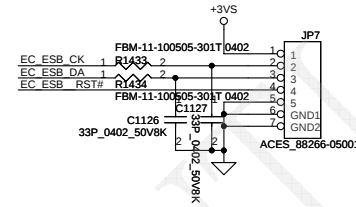
CAP Sensor left(ENE)



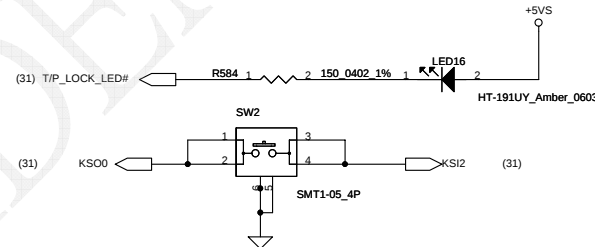
CAP Sensor right(Cypress)



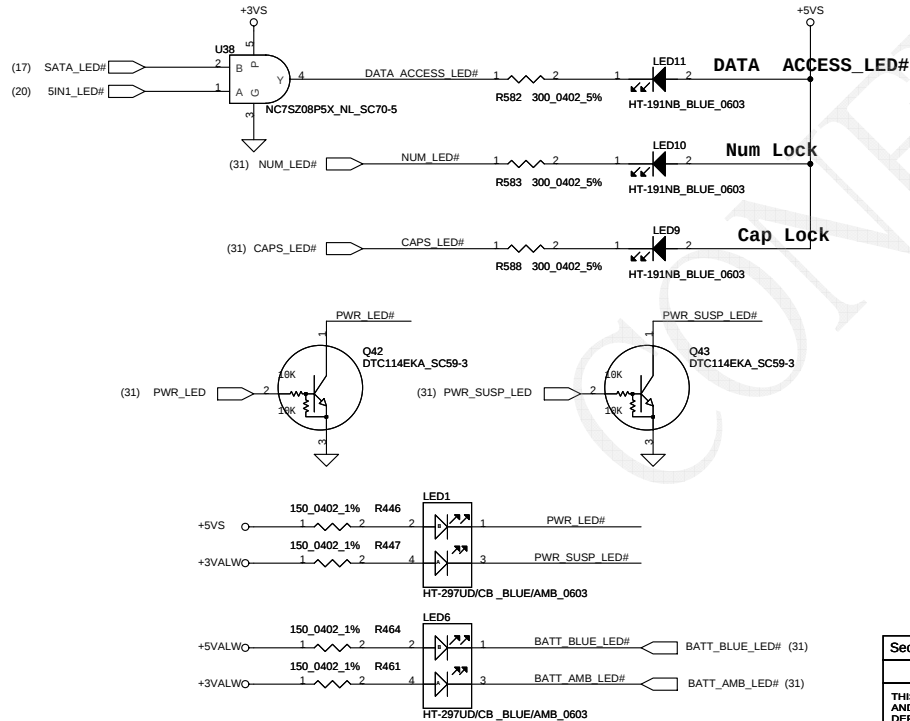
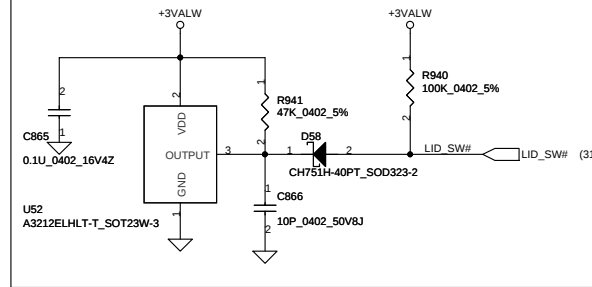
CAP Sensor B COVER(ENE)



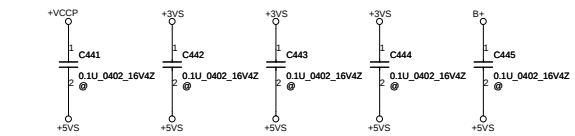
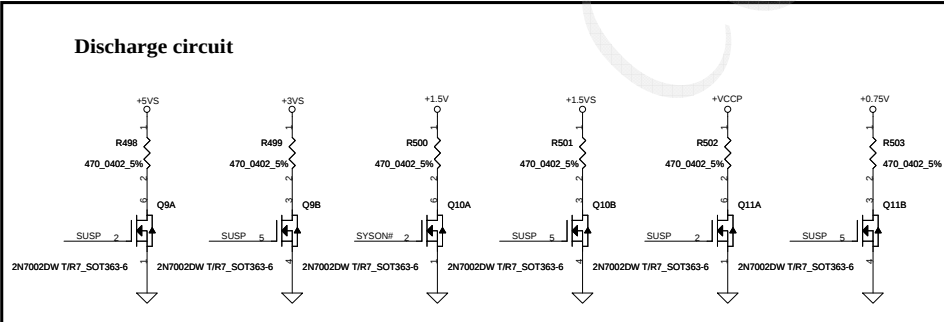
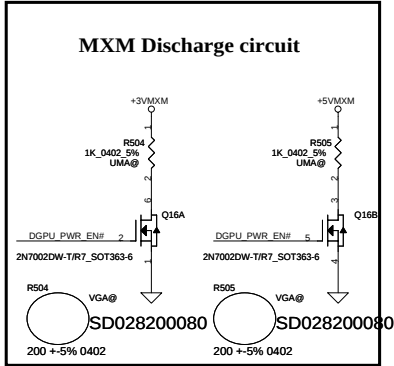
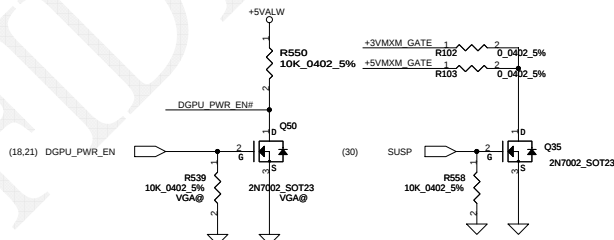
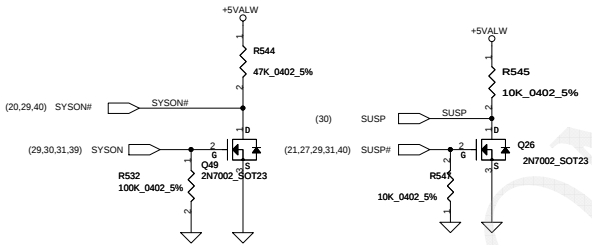
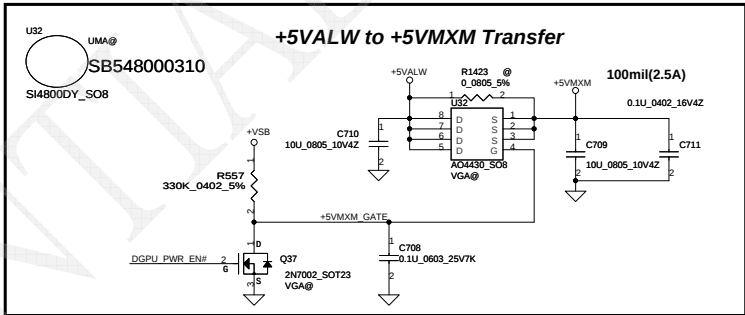
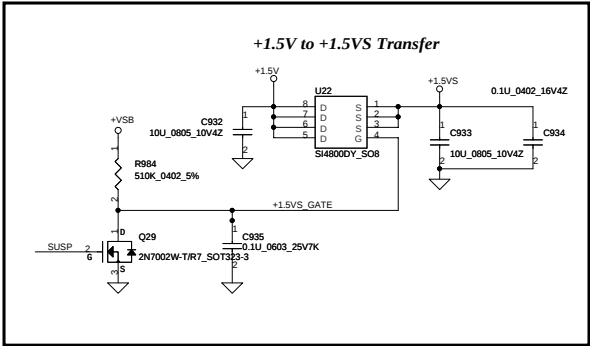
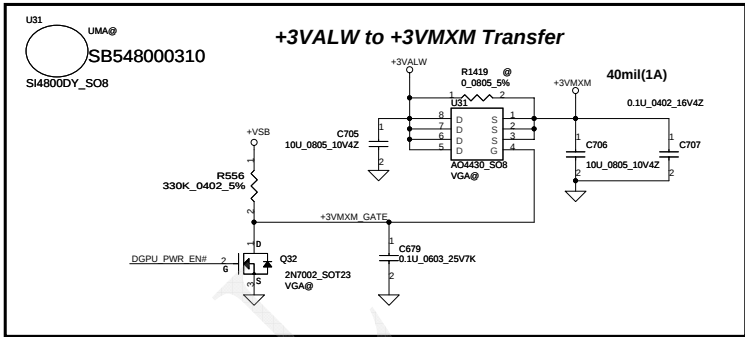
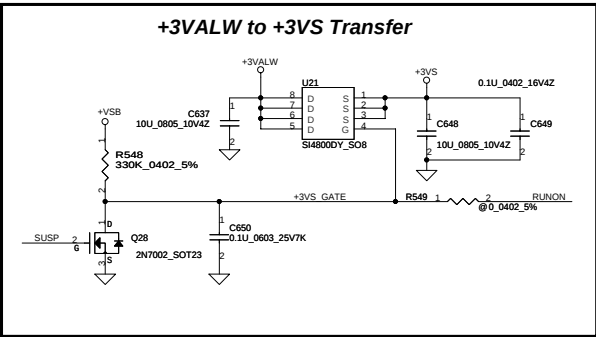
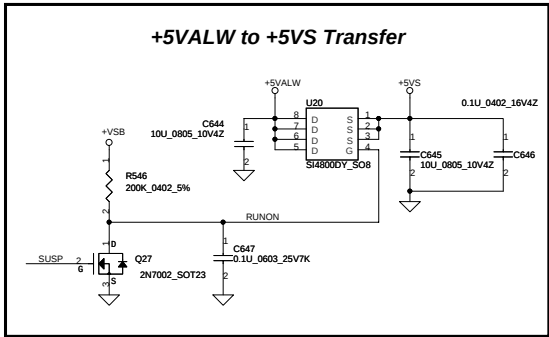
T/P Lock Button/LED

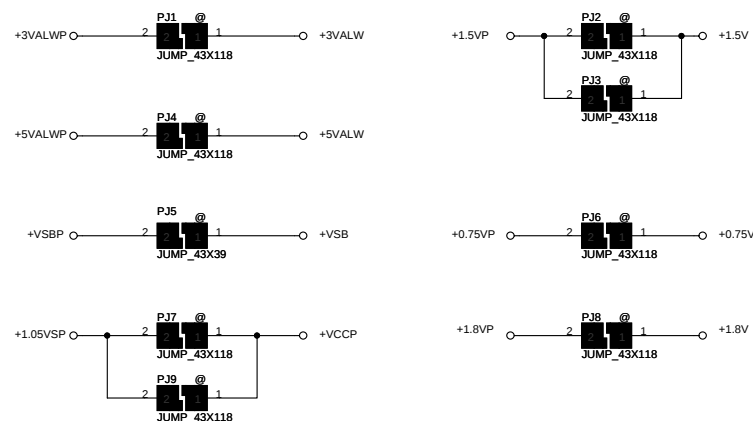
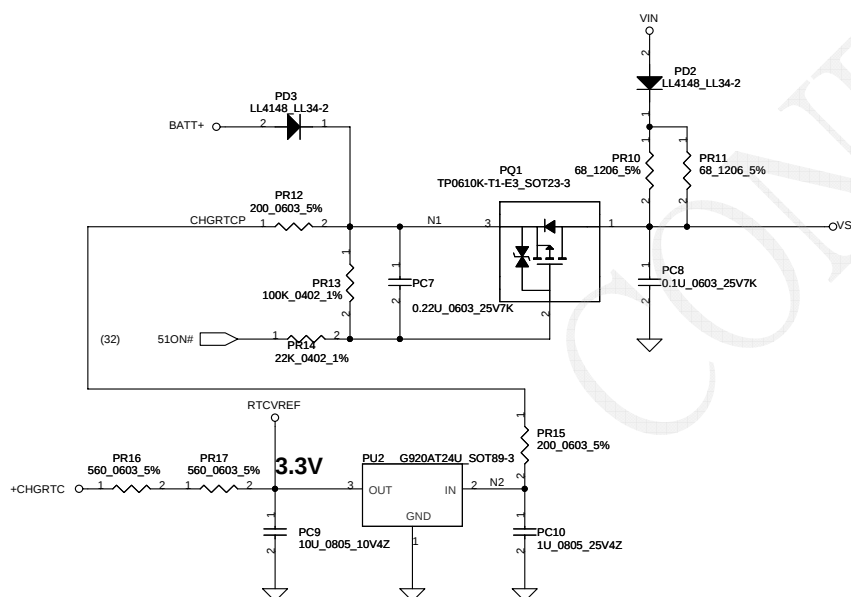
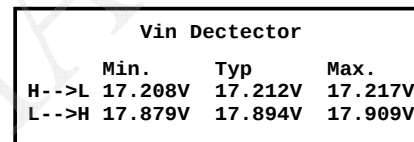
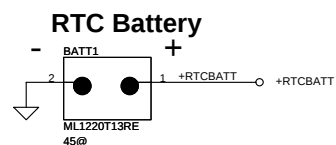


(Hall Effect Switch)



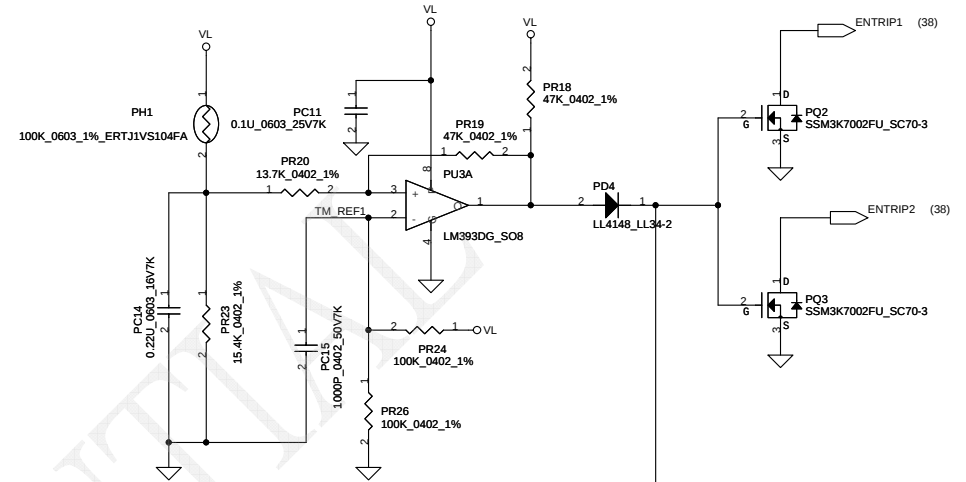
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				401673
				Rev B
				Date: Thursday, April 30, 2009
				Sheet 33 of 43



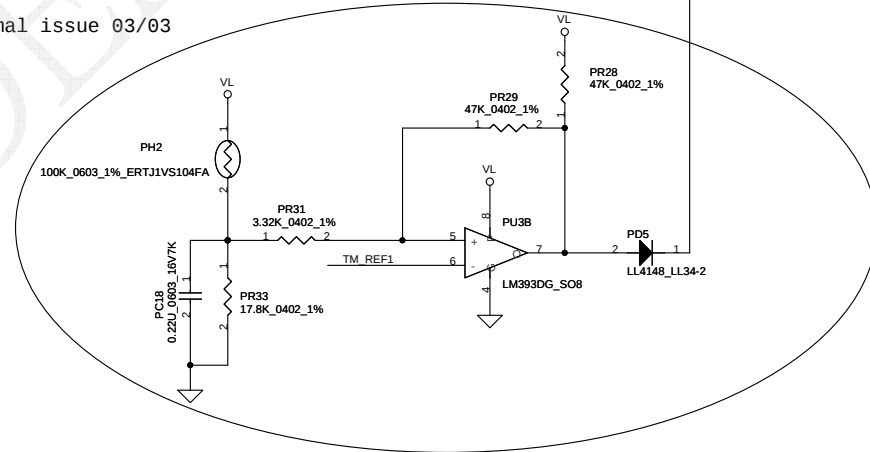


Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date		2007/09/29	Deciphered Date	2007/09/29	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					SCHEMATIC, MB A5011	
					Date: Thursday, April 30, 2009 Document Number: 401673 Sheet: 35 of 43 Rev: B	

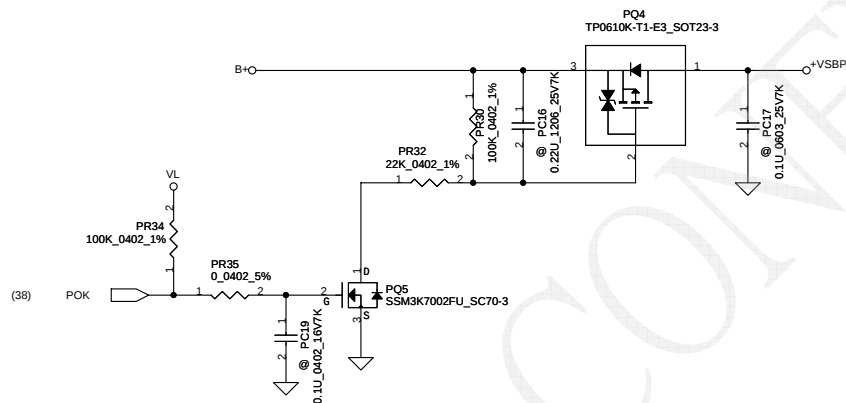
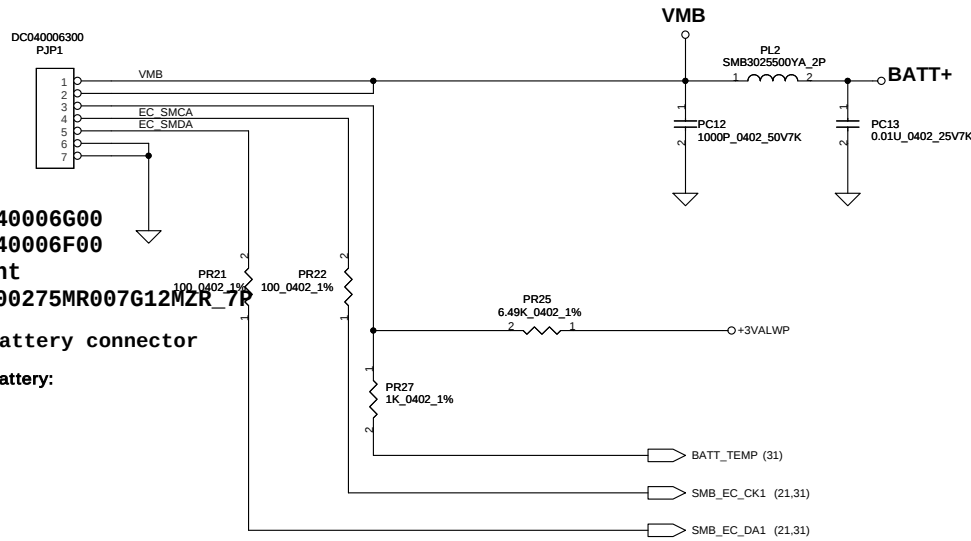
PH1 under CPU botten side :
CPU thermal protection at 92 degree C
Recovery at 56 degree C



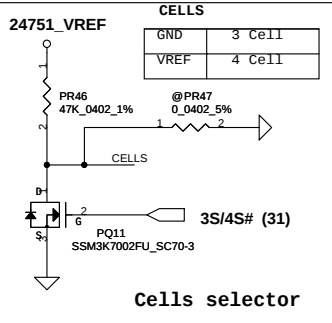
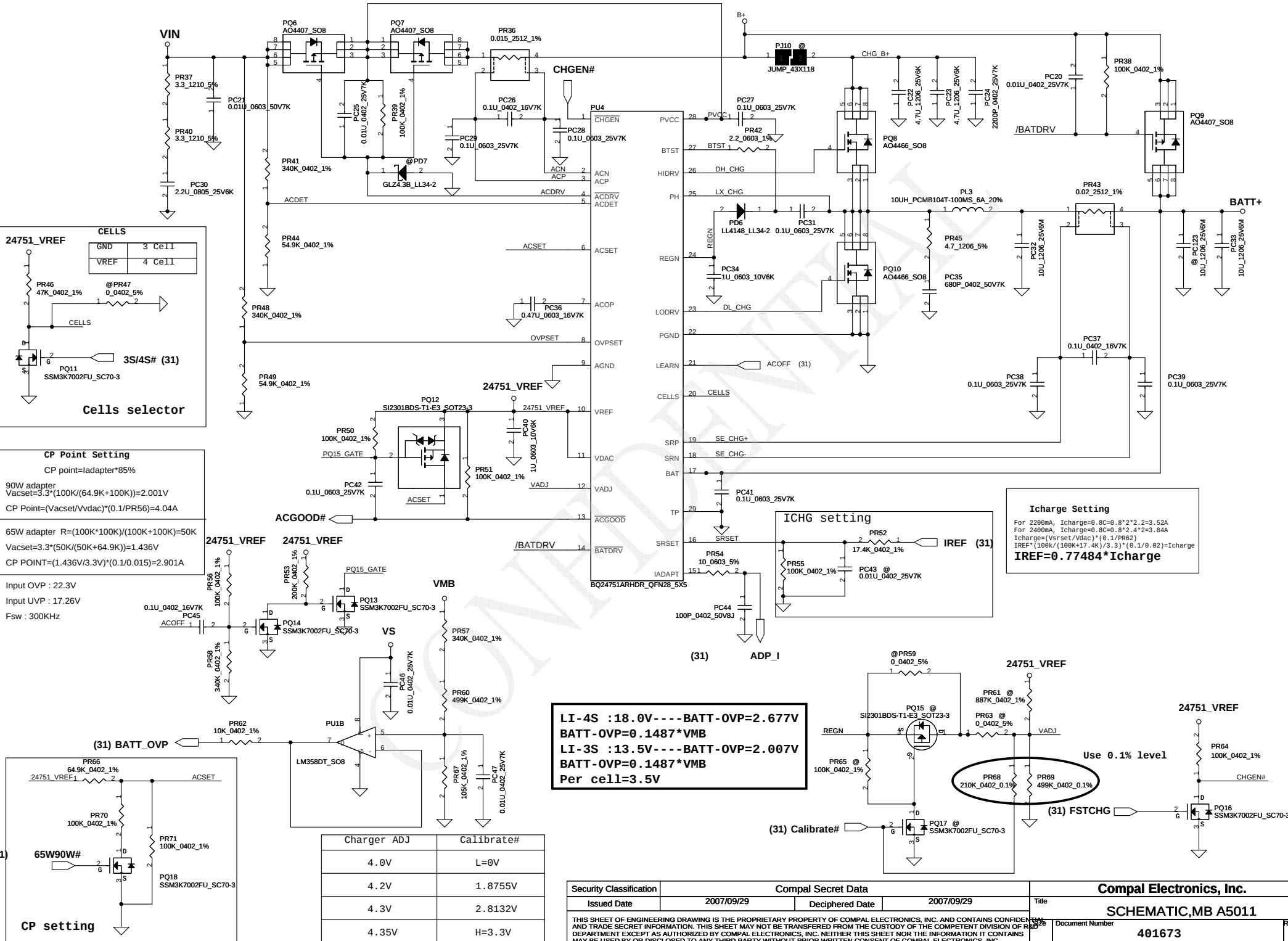
PH2 near main Battery CONN :
BAT. thermal protection at 76 degree C
Recovery at 72 degree C



solve battery thermal issue 03/03



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Title	SCHEMATIC,MB A5011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401673
				Date: Friday, May 15, 2009	Sheet 36 of 43

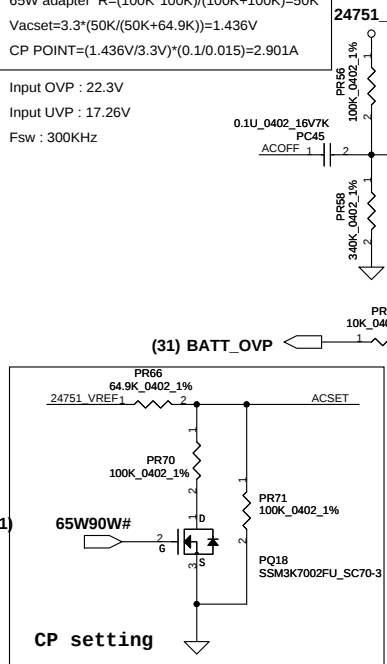


CP Point Setting
CP point=ladapter*85%

90W adapter
 $V_{acset}=3.3 \times (100K / (64.9K + 100K)) = 2.001V$
CP Point=(V_{acset} / V_{vdac})*(0.1/PR56)=4.04A

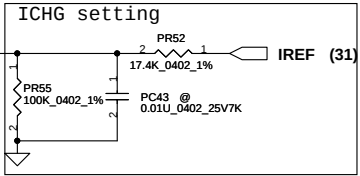
65W adapter $R=(100K \times 100K) / (100K + 100K) = 50K$
 $V_{acset}=3.3 \times (50K / (50K + 64.9K)) = 1.436V$
CP POINT=($1.436V / 3.3V$)*(0.1/0.015)=2.901A

Input OVP : 22.3V
Input UVP : 17.26V
Fsw : 300KHz

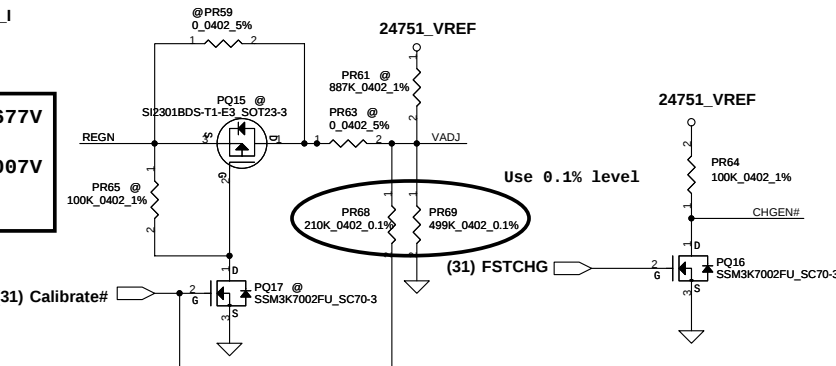


Charger ADJ	Calibrate#
4.0V	L=0V
4.2V	1.8755V
4.3V	2.8132V
4.35V	H=3.3V

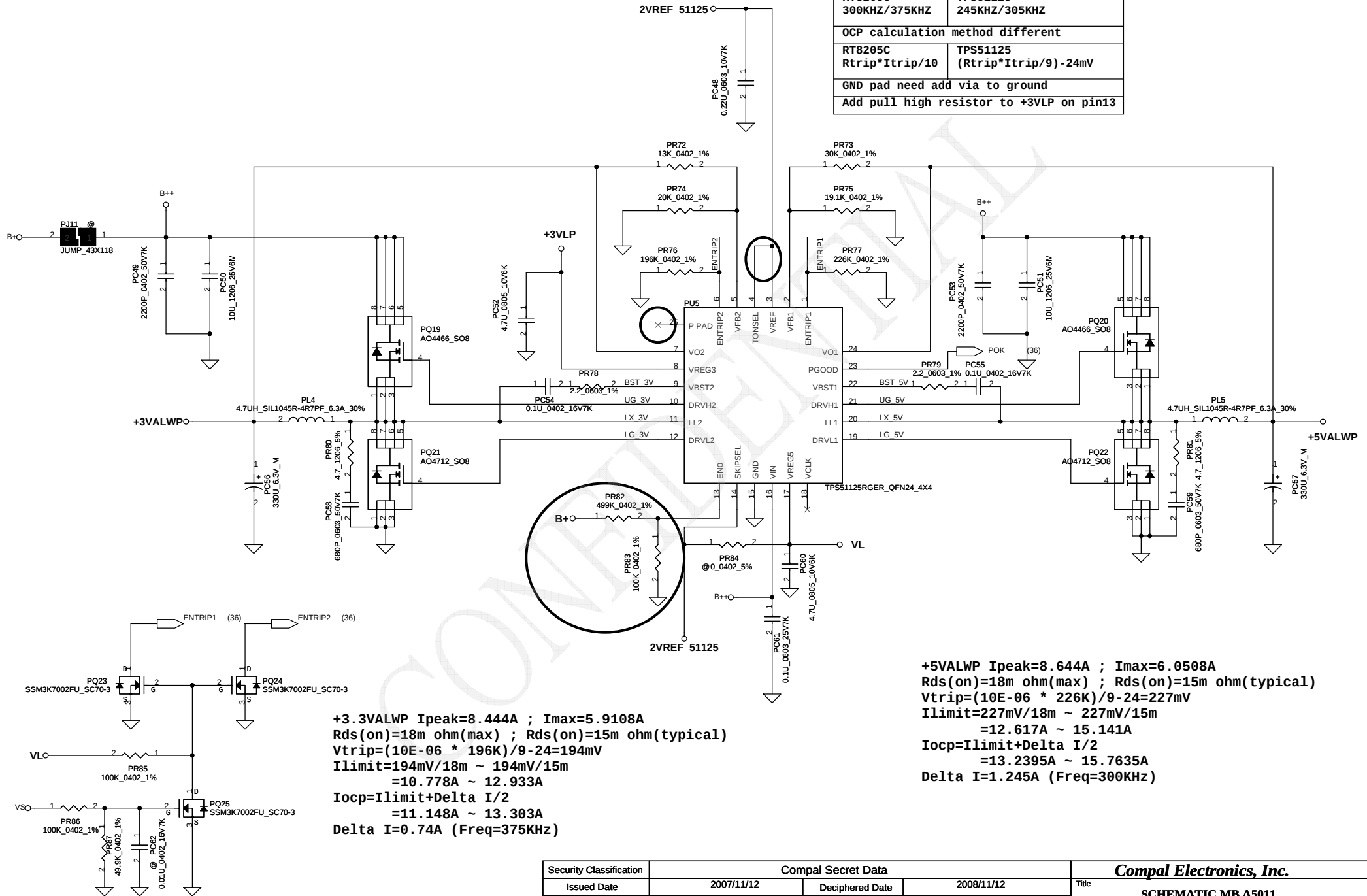
LI-4S : 18.0V --- BATT-OVP=2.677V
BATT-OVP=0.1487*VMB
LI-3S : 13.5V --- BATT-OVP=2.007V
BATT-OVP=0.1487*VMB
Per cell=3.5V



Icharge Setting
For 2200mA, Icharge=0.8C=0.8*2*2.2=3.52A
For 2400mA, Icharge=0.8C=0.8*2.4*2=3.84A
Icharge=(V_{srset} / V_{dac})*(0.1/PR62)
IREF*(100K/(100K+17.4K)/3.3)*(0.1/0.02)=Icharge
IREF=0.77484*Icharge



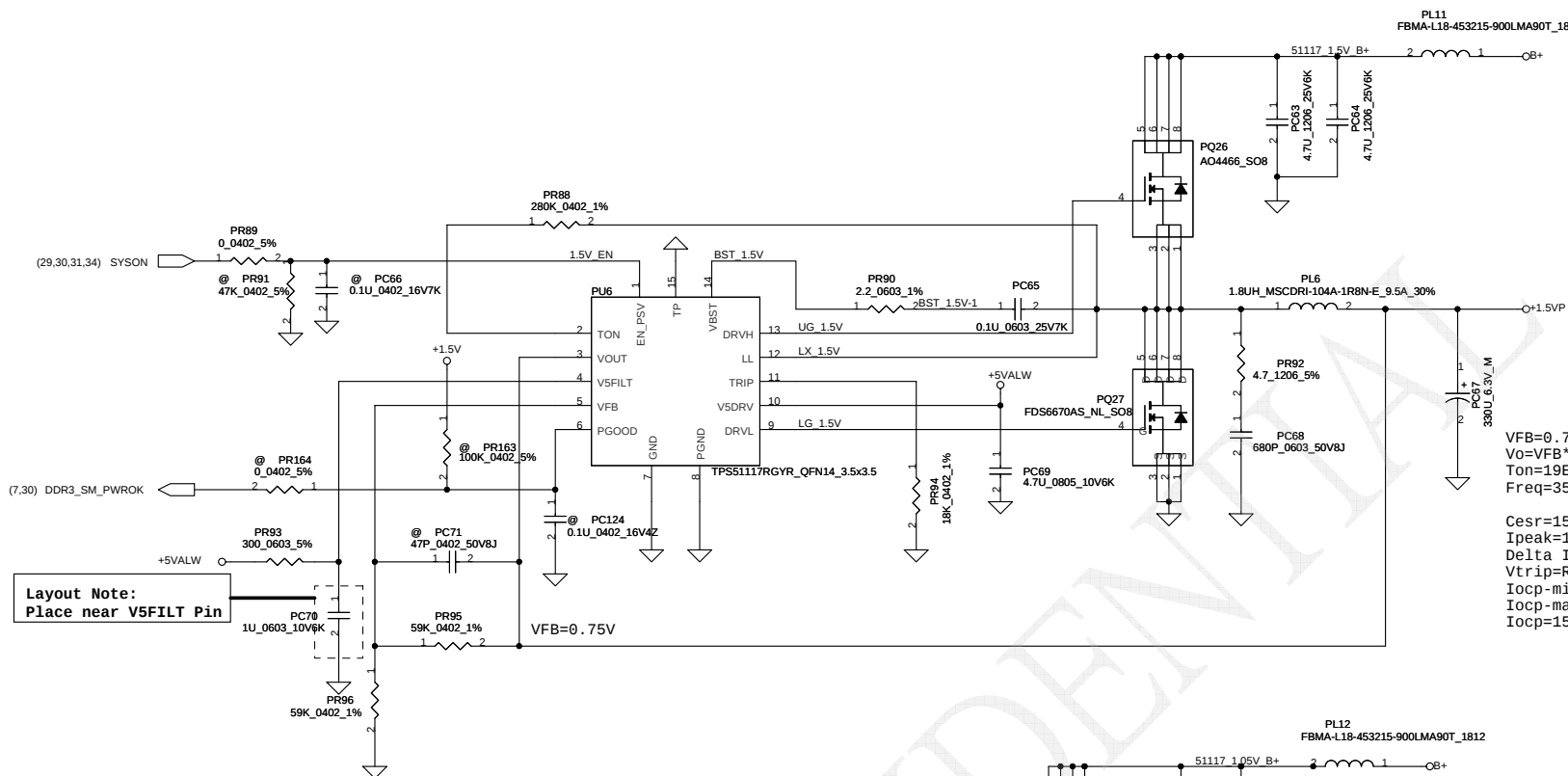
Frequency different	
RT8205C 300KHZ/375KHZ	TPS51125 245KHZ/305KHZ
OCP calculation method different	
RT8205C Rtrip*Itrip/10	TPS51125 (Rtrip*Itrip/9)-24mV
GND pad need add via to ground	
Add pull high resistor to +3VLP on pin13	



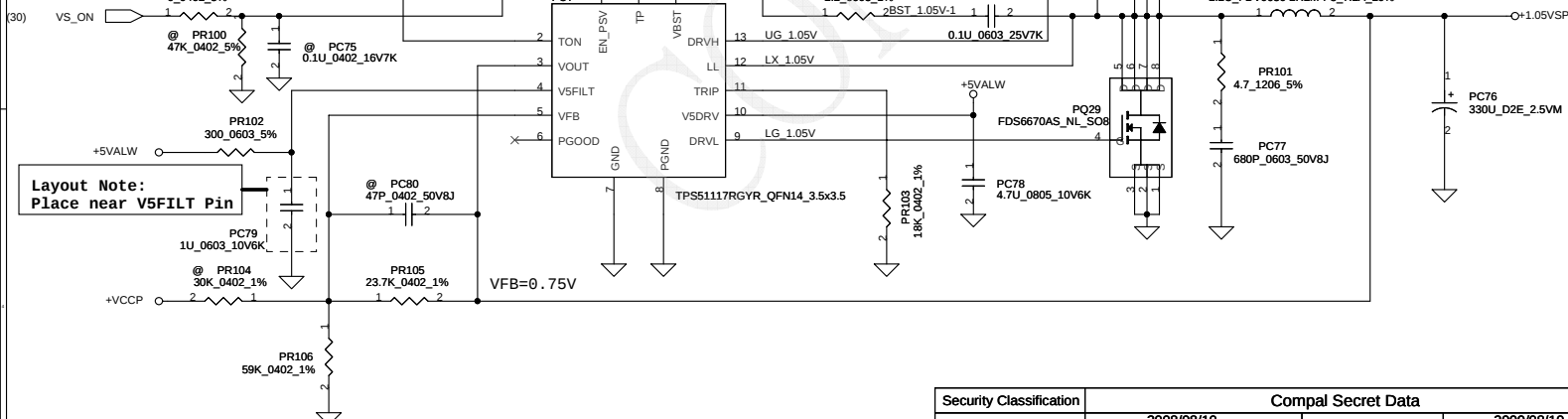
+3.3VALWP Ipeak=8.444A ; Imax=5.9108A
Rds(on)=18m ohm(max) ; Rds(on)=15m ohm(typical)
Vtrip=(10E-06 * 196K)/9-24=194mV
Ilimit=194mV/18m ~ 194mV/15m
=10.778A ~ 12.933A
Iocp=Ilimit+Delta I/2
=11.148A ~ 13.303A
Delta I=0.74A (Freq=375KHz)

+5VALWP Ipeak=8.644A ; Imax=6.0508A
Rds(on)=18m ohm(max) ; Rds(on)=15m ohm(typical)
Vtrip=(10E-06 * 226K)/9-24=227mV
Ilimit=227mV/18m ~ 227mV/15m
=12.617A ~ 15.141A
Iocp=Ilimit+Delta I/2
=13.2395A ~ 15.7635A
Delta I=1.245A (Freq=300KHz)

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/11/12	Deciphered Date	2008/11/12	Title	SCHEMATIC,MB A5011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401673
				Date	Thursday, April 30, 2009
				Sheet	38 of 43

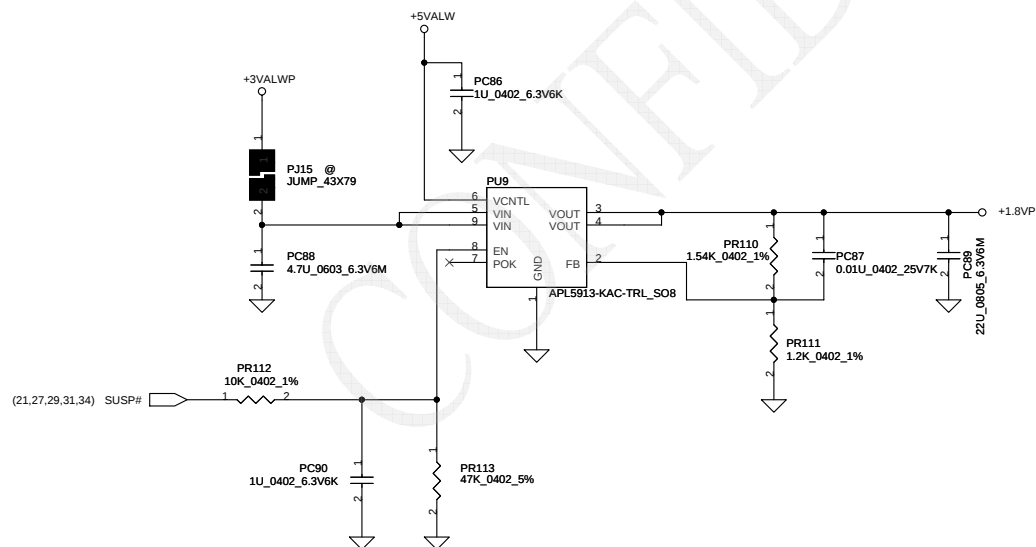
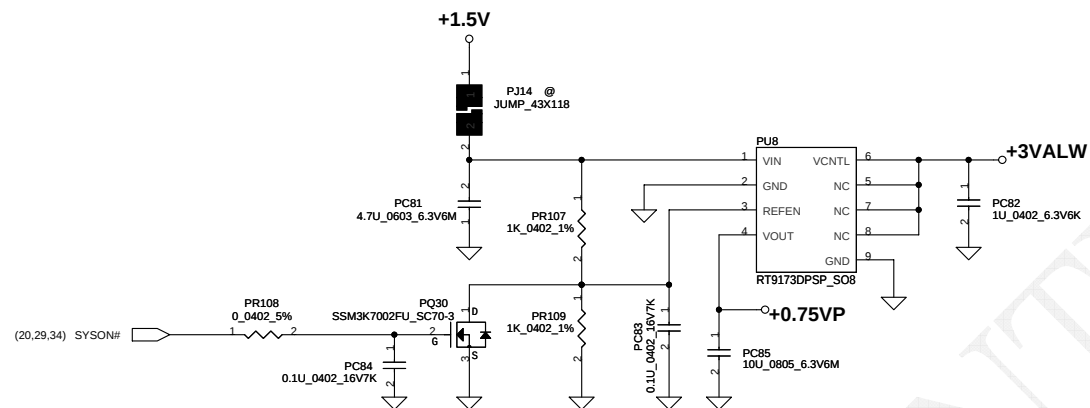


$V_{FB}=0.75V$
 $V_o=V_{FB} \cdot (1+PR95/PR96)=1.5V$
 $T_{on}=19E-12 \cdot R_{on} \cdot (((2/3) \cdot V_o+150mV)/V_{in})+50ns=4.68E-7$
 $Freq=356KHz$
 $C_{esr}=15m\ ohm$
 $I_{peak}=13.67A$ $I_{max}=9.569A$
 $\Delta I=((19.5-1.5) \cdot (1.5/19.5))/(L \cdot Freq)=2.429A$
 $V_{trip}=R_{trip} \cdot I_{0uA}=0.180V$
 $I_{ocp-min}=V_{trip}/R_{dsonmax} \cdot 1.1+2.429=15.161A$
 $I_{ocp-max}=V_{trip}/R_{dsonmax} \cdot 1.1+2.429=23.436A$
 $I_{ocp}=15.161-23.436A$

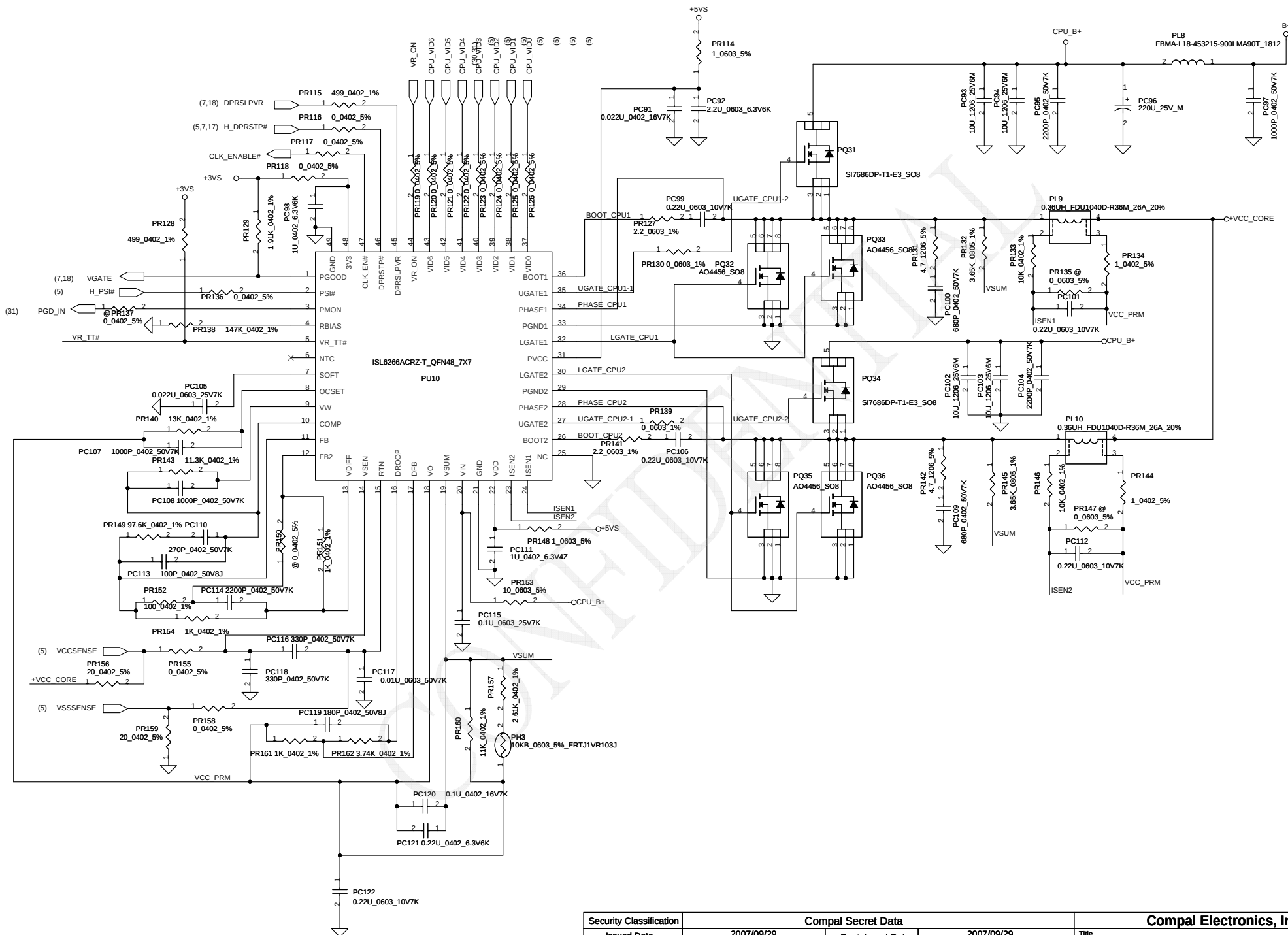


$V_{FB}=0.75V$
 $V_o=V_{FB} \cdot (1+PR105/PR106)=1.05V$
 $T_{on}=19E-12 \cdot R_{on} \cdot (((2/3) \cdot V_o+150mV)/V_{in})+50ns=3.54E-07$
 $Freq=330KHz$
 $C_{esr}=15m\ ohm$
 $I_{peak}=17.47A$ $I_{max}=12.229A$
 $\Delta I=((19.5-1.05) \cdot (1.05/19.5))/(L \cdot Freq)=1.597A$
 $V_{trip}=R_{trip} \cdot I_{0uA}=0.180V$
 $I_{ocp-min}=V_{trip}/R_{dsonmax} \cdot 1.1+1.597=14.745A$
 $I_{ocp-max}=V_{trip}/R_{dsonmax} \cdot 1.1+1.597=23.019A$
 $I_{ocp}=14.745-23.019A$

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/08/10	Deciphered Date	2009/08/10	Title	SCHEMATIC,MB A5011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401673
				Date	Thursday, April 30, 2009
				Sheet	39 of 43



Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Title	SCHEMATIC, MB A5011	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Doc Number	401673	Rev B
				Date: Thursday, April 30, 2009	Sheet 40 of 43	



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SCHEMATIC,MB A5011
401673				Rev B
Date: Thursday, April 30, 2009				Sheet 41 of 43

Version change list (P.I.R. List)

Page 1 of 2
for PWR

Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	Date	Phase
1		Unite parts			PQ5,PQ11,PQ13,PQ14,PQ16,PQ17,PQ18,PQ30 Change from SB000006800、SB000009080 to SB000009610	2008 12/19	EVT
2		EMI request			Add PR80,PR81,PR131,PR142 SD001470B80 (S RES 1/4W 4.7 +-5% 1206)	2008 12/19	EVT
3		EMI request			Change PR78,PR79,PR127,PR141 from 0 to 2.2 (SD014220B80)	2008 12/19	EVT
4		Reduse 1.5V, 1.05V frequency to about 300KHz			Change PR88,PR97 from 180K to 280K(SD034280380)	2008 12/19	EVT
5	Output voltage error	Set 1.8V voltage			Change PR111 from 1.71K to 1.2K(SD034120180)	2008 12/19	EVT
6		Down size			Change PC88 from 0805 to 0603(SE093475K80)	2008 12/19	EVT
7	Heavy load efficiency fail	Raise heavy load efficiency		38	Change PL4,PL5 from 10uF to 4.7uF(SH000006J80)	2008 12/19	EVT
8		EMI request			Add PC58,PC59 S CER CAP 680P 50V K X7R 0603 (SE025681K80)	2008 12/19	EVT
9		EMI request			Add PC100,PC109 S CER CAP 680P 50V K X7R 0402 (SE074681K80)	2008 12/19	EVT
10				41	Change PR143 from 8.25K to 11.3K(SD034113280)	2008 12/19	EVT
11		Improve load line		41	Change PR162 from 4.42K to 3.74K(SD034374180)	2008 12/22	EVT
12		New charge voltage setting		37	Delete PQ15,PQ17,PR61,PR63,PR65	2008 12/23	EVT
13		New charge voltage setting		37	Add PR68 210K_0402_1%(SD034210380)	2008 12/23	EVT
14		New charge voltage setting		37	Change PR69 from 221K to 49.9K(SD034499280)	2008 12/23	EVT
15	Battery can't charge to 100%			37	Change PR69 from 49.9K to 499K(SD034499380)	2009 01/12	DVT
16	HDMI 5V voltage test fail(should 4.8~5.3V)	Raise 5V voltage		38	Change PR75 from 20K to 19.1K(SD034191280)	2009 01/17	DVT
17		Prevent spike		37	Add PD7	2009 01/21	DVT
18		Improve charge voltage		37	Change PR68, PR69 from 1% to 0.1%	2009 01/22	DVT
19		EMI request		39	Change PR90, PR99 from 0 to 2.2(SD014220B80) Enable PR92, PR101, PC68, PC77	2009 02/04	DVT
20		EMI request		39	Change PJ12 to PL11(SM010020720) Change PJ13 to PL12(SM010020720)	2009 02/04	DVT
21		EMI request		37	Change PR42 from 0 to 2.2(SD014220B80) Enable PR45, PC35	2009 02/04	DVT
22	Protect logic low	Set trigger point at 76		36	Enable PD5, PH2, PR28, PR29, PR31, PR33, PC18	2009 02/25	PVT
23							

PROPRIETARY NOTE: THIS SHEET OF ENGINEERING DRAWING AND SPECIFICATIONS CONTAINS CONFIDENTIAL
TRADE SECRET AND OTHER PROPRIETARY INFORMATION OF DELL INC. ("DELL") THIS DOCUMENT MAY NOT
BE TRANSFERRED OR COPIED WITHOUT THE EXPRESS WRITTEN AUTHORIZATION OF DELL. IN ADDITION,
NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD
PARTY WITHOUT DELL'S EXPRESS WRITTEN CONSENT.

Compal Electronics, Inc.

Title

SCHEMATIC,MB A5011

Size

Document Number

401673

Rev

B

Date: Thursday, April 30, 2009

Sheet 42 of 43

Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	Date	Phase
24							
25							
26							
27							
28							
29							
30							
31							
32							
33							
34							
35							
36							
37							
38							
39							
40							
41							
42							
43							
44							
45							
46							

PROPRIETARY NOTE: THIS SHEET OF ENGINEERING DRAWING AND SPECIFICATIONS CONTAINS CONFIDENTIAL TRADE SECRET AND OTHER PROPRIETARY INFORMATION OF DELL INC. ("DELL") THIS DOCUMENT MAY NOT BE TRANSFERRED OR COPIED WITHOUT THE EXPRESS WRITTEN AUTHORIZATION OF DELL. IN ADDITION, NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT DELL'S EXPRESS WRITTEN CONSENT.

Compal Electronics, Inc.			
Title			
SCHEMATIC,MB A5011			
Size	Document Number	401673	Rev B
Date:	Thursday, April 30, 2009	Sheet	43 of 43

www.s-manuals.com